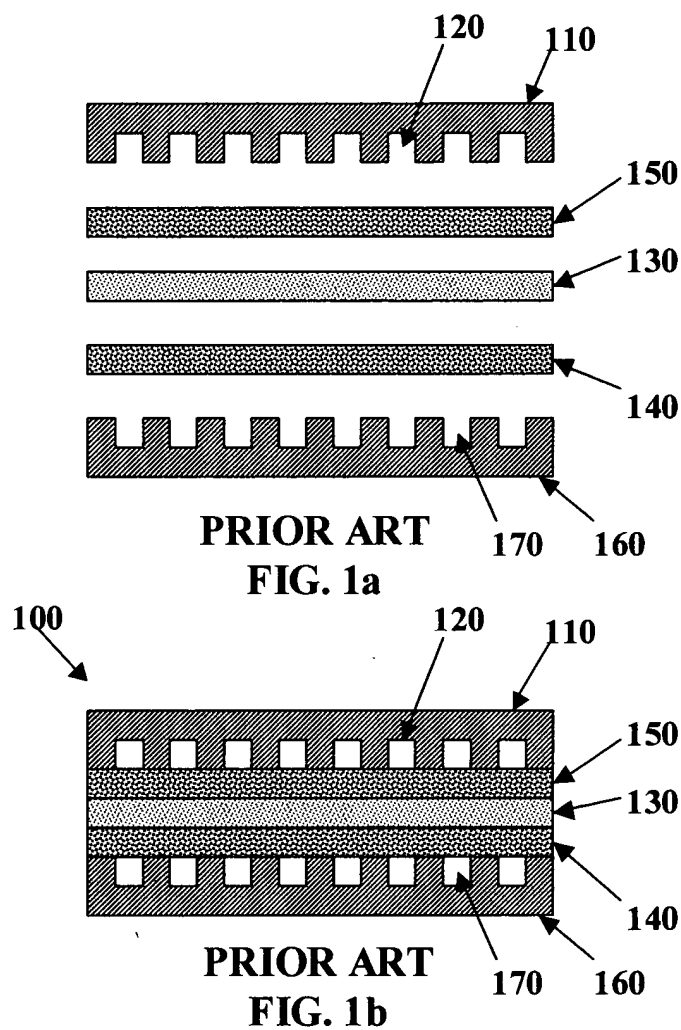


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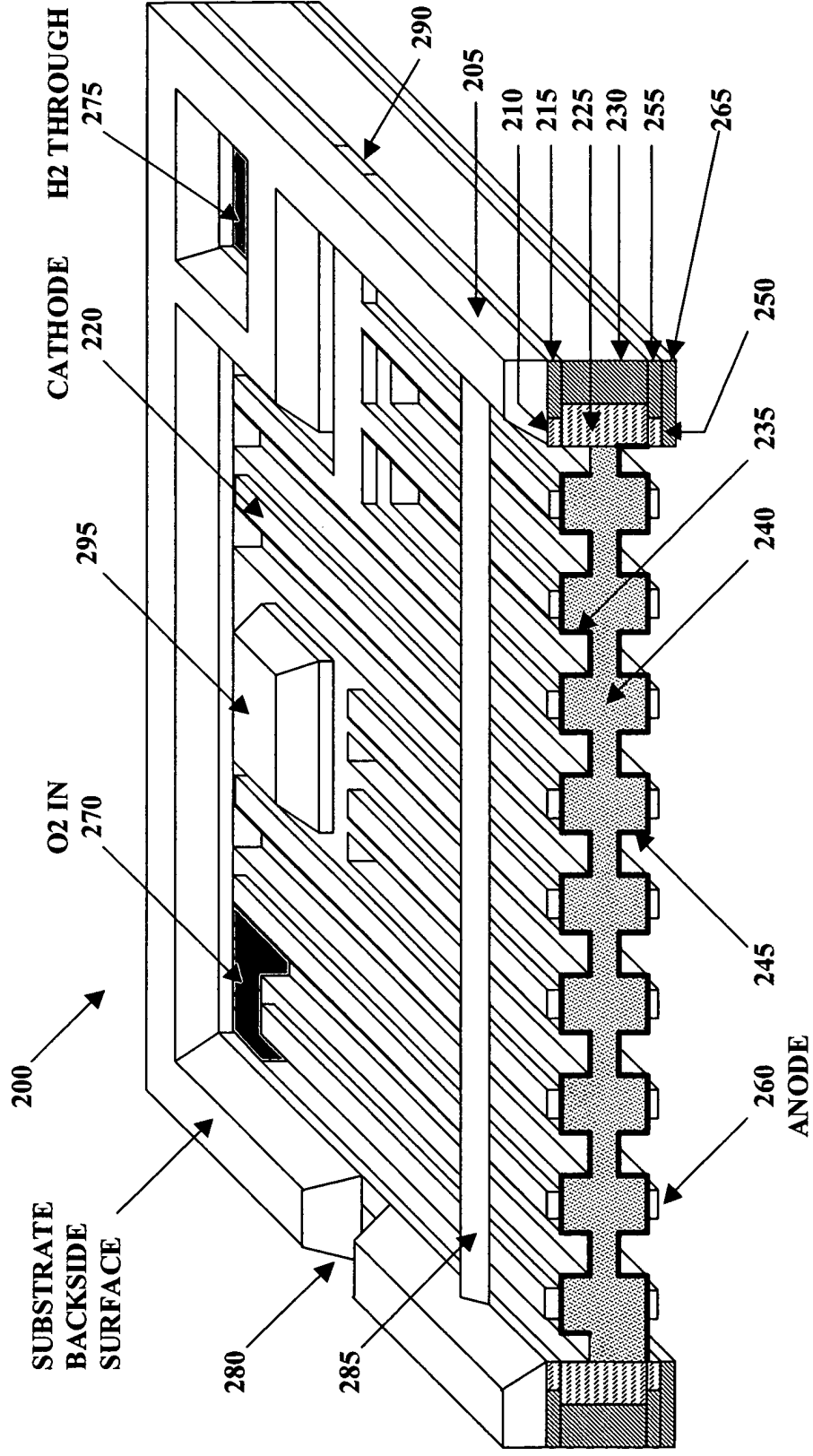
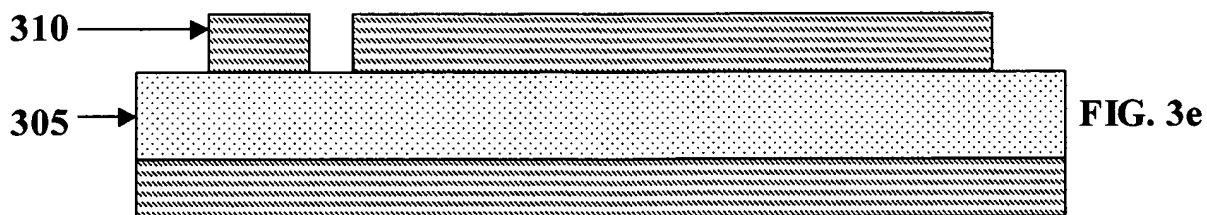
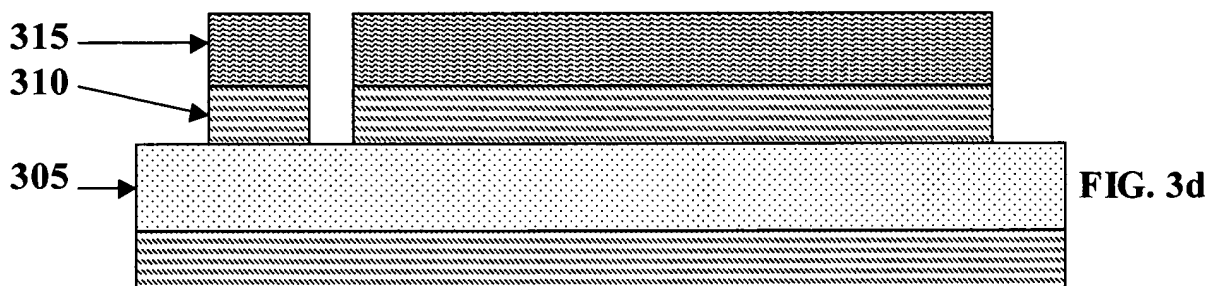
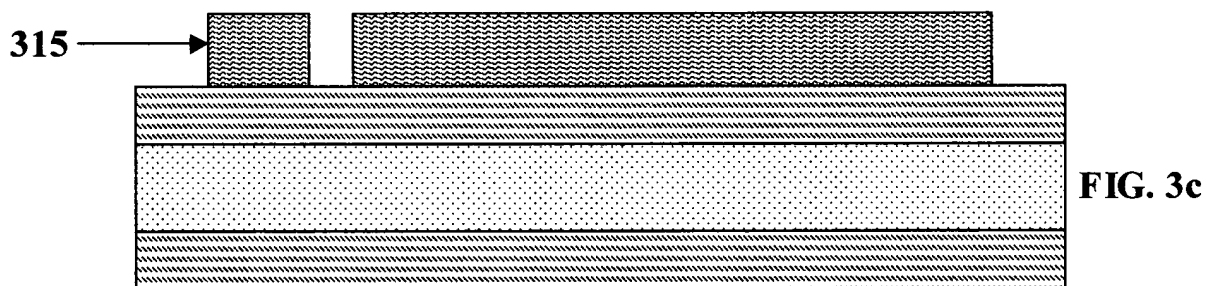
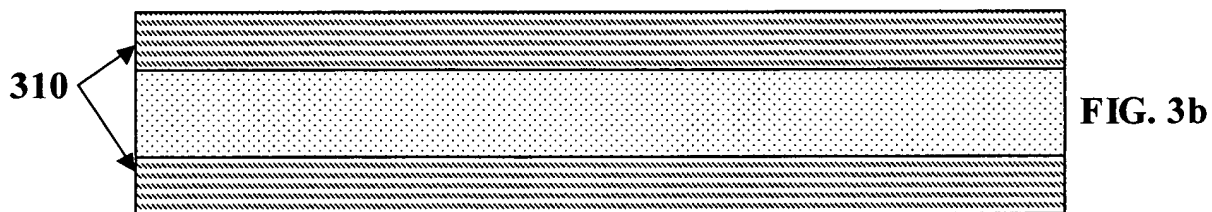
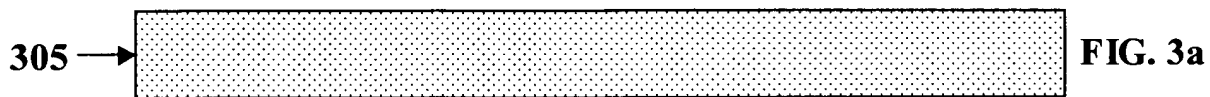
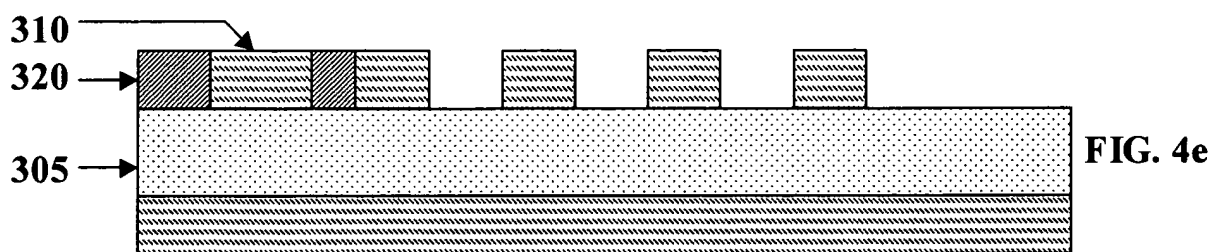
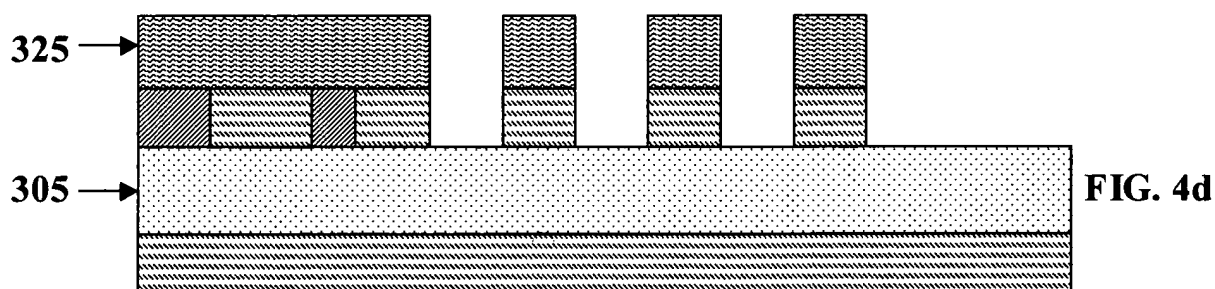
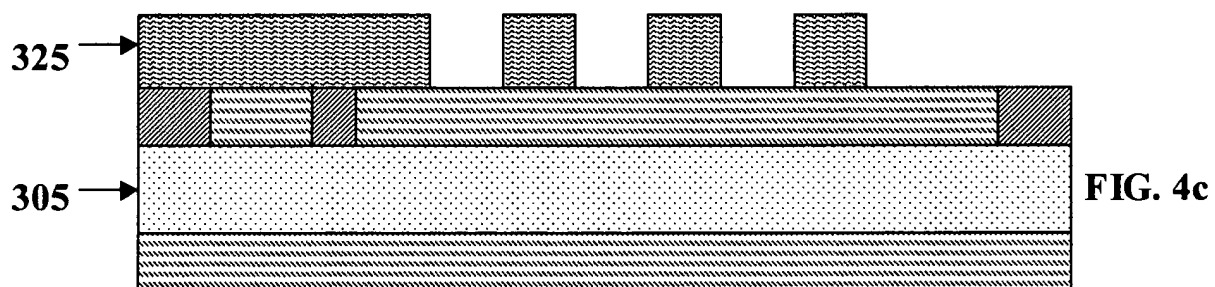
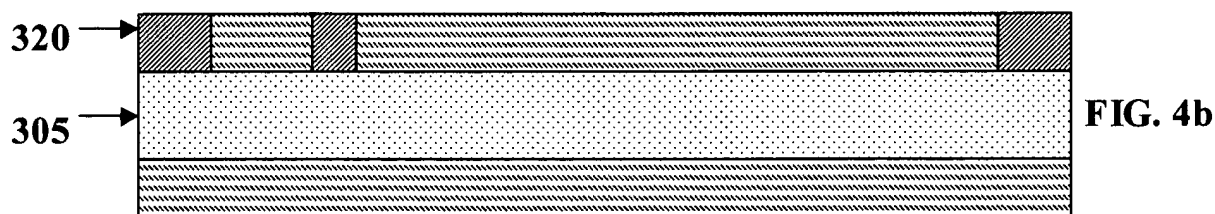
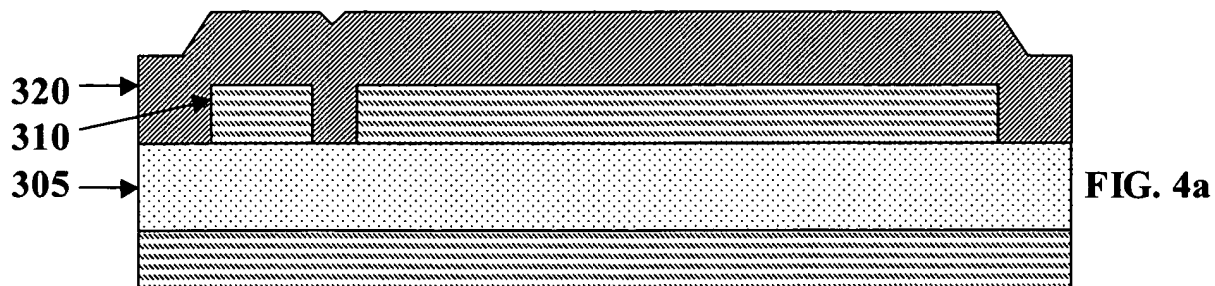


FIG. 2

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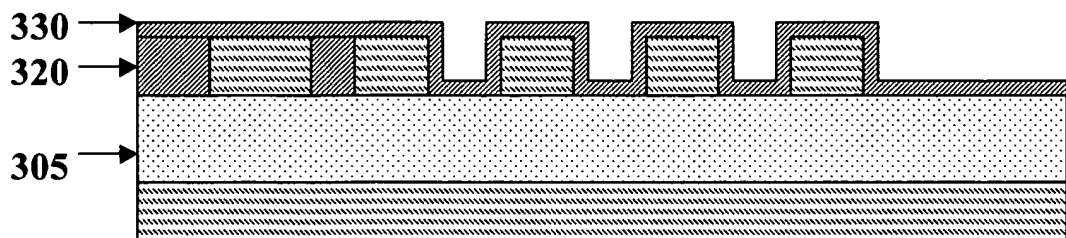


FIG. 5a

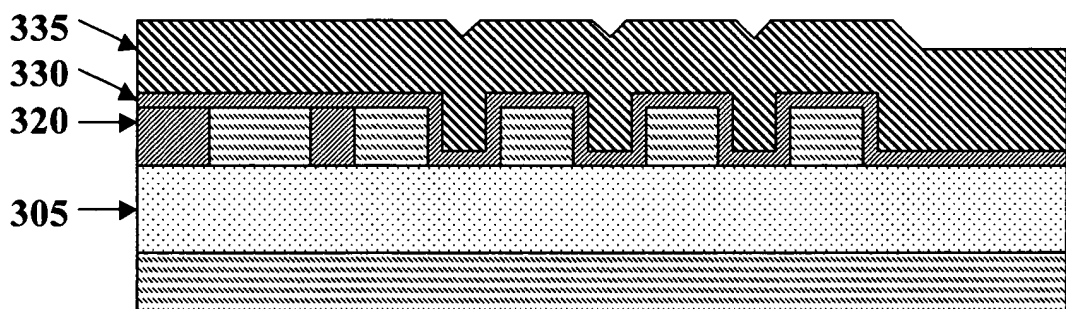


FIG. 5b

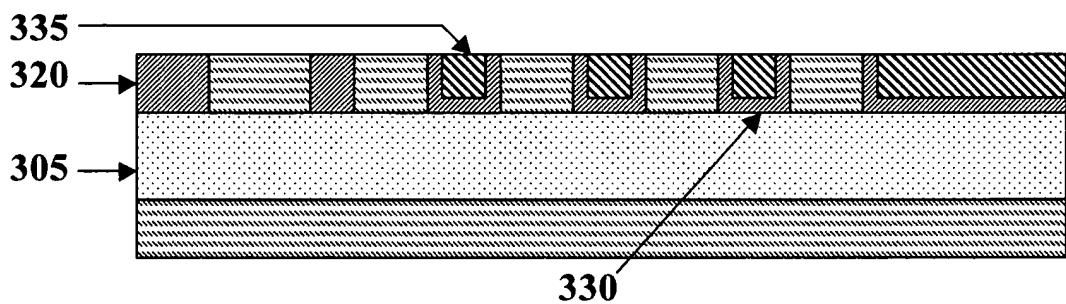


FIG. 5c

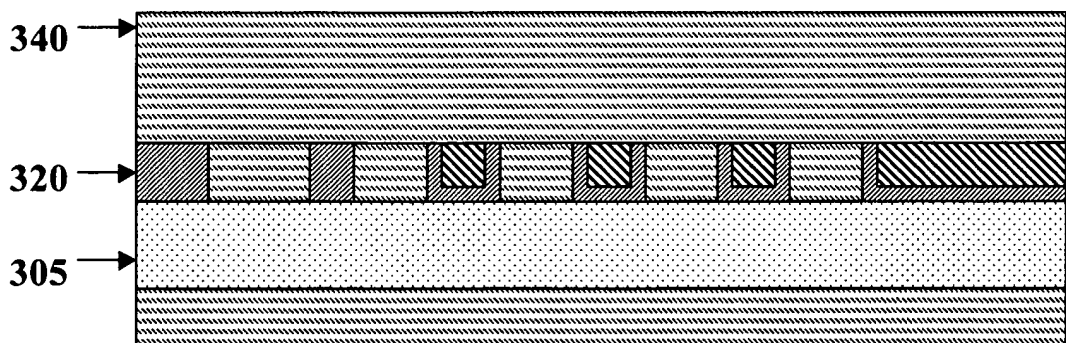


FIG. 5d

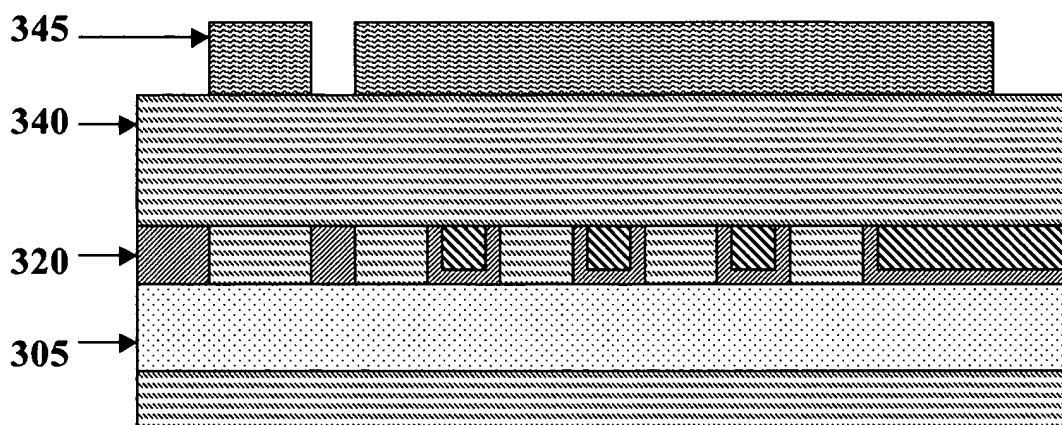


FIG. 6a

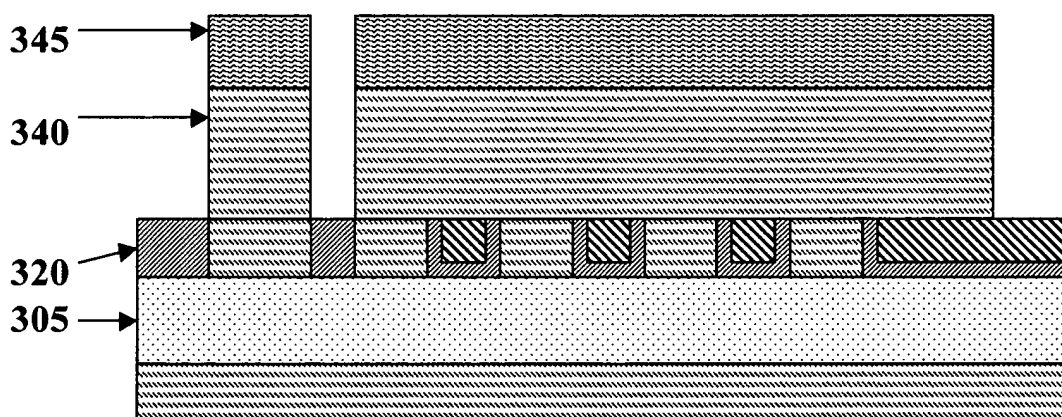


FIG. 6b

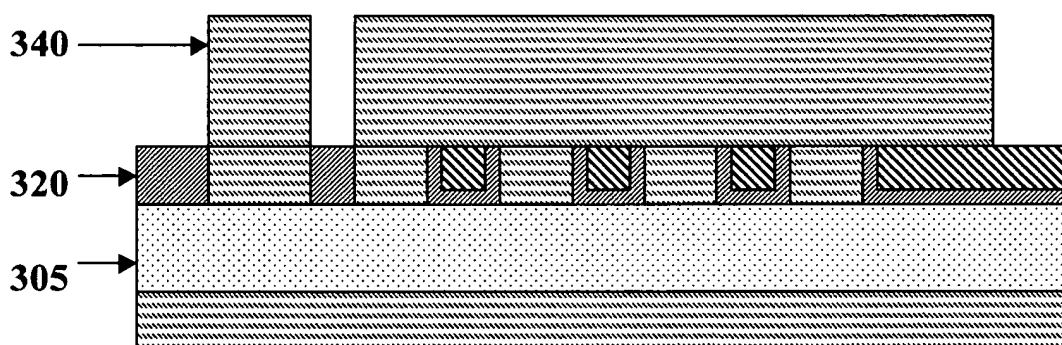


FIG. 6c

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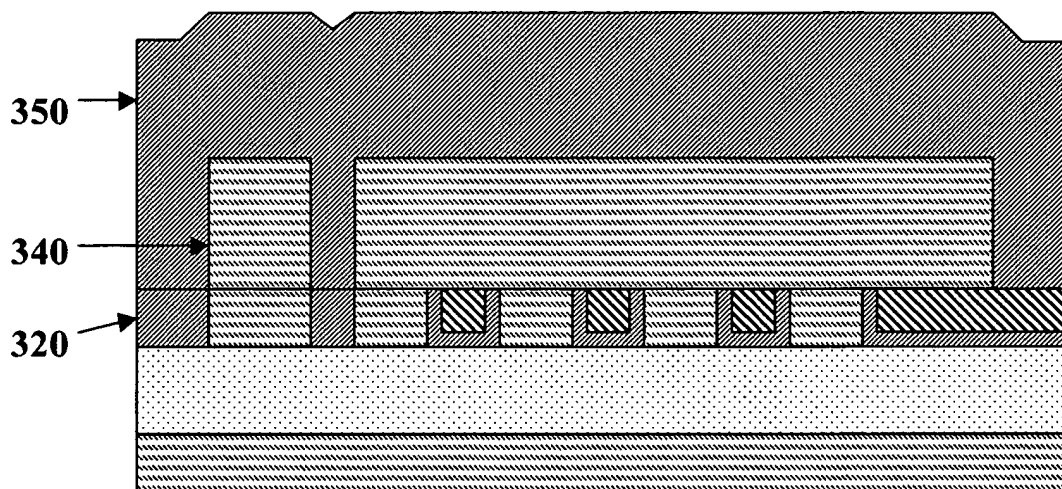


FIG. 7a

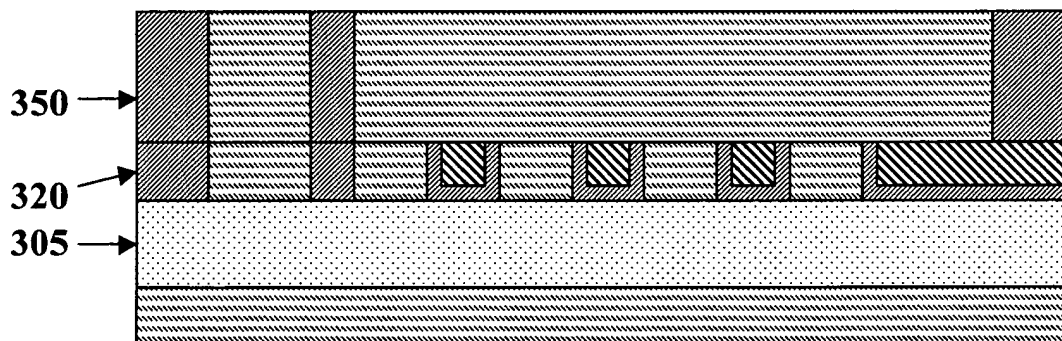


FIG. 7b

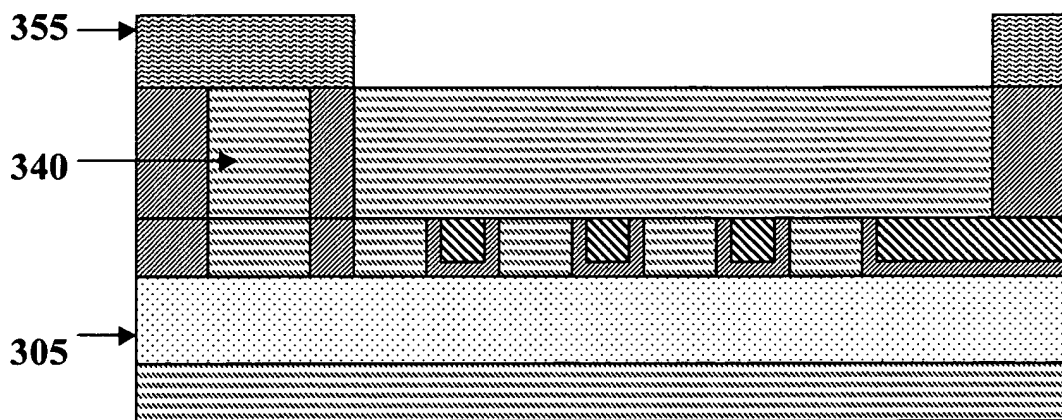


FIG. 7c

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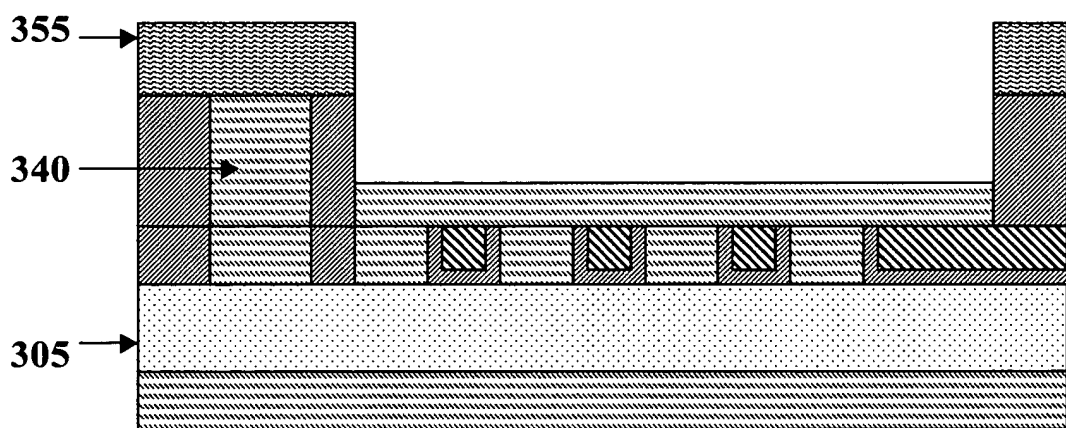


FIG. 8a

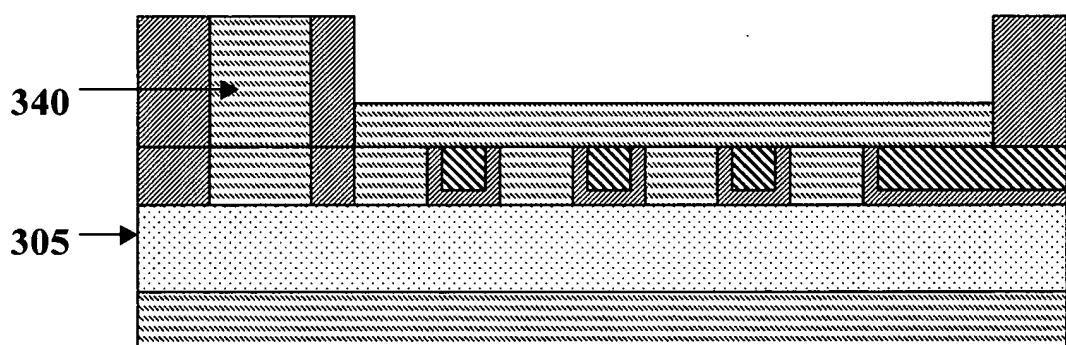


FIG. 8b

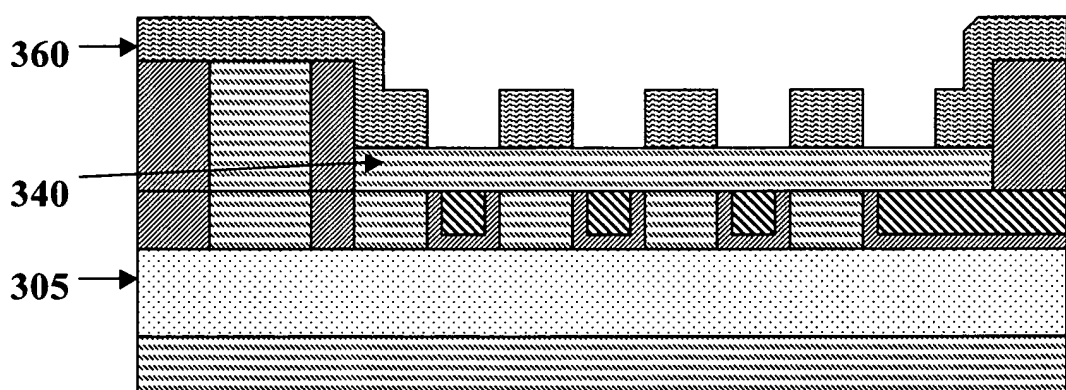


FIG. 8c

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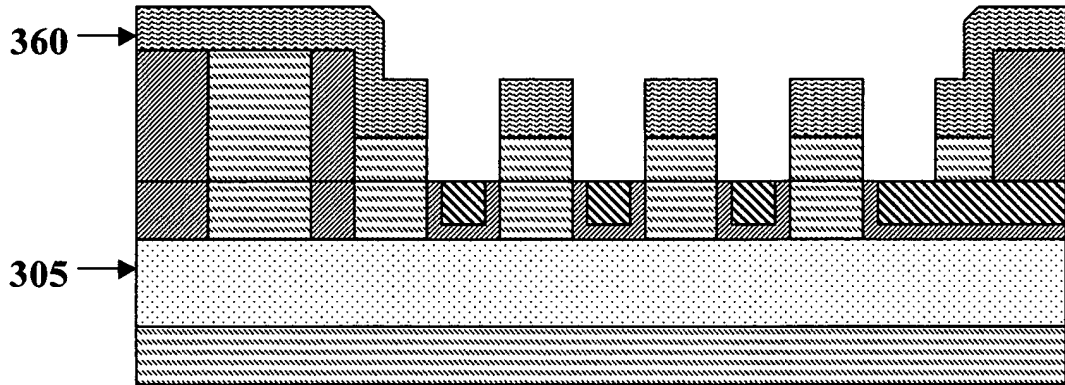


FIG. 9a

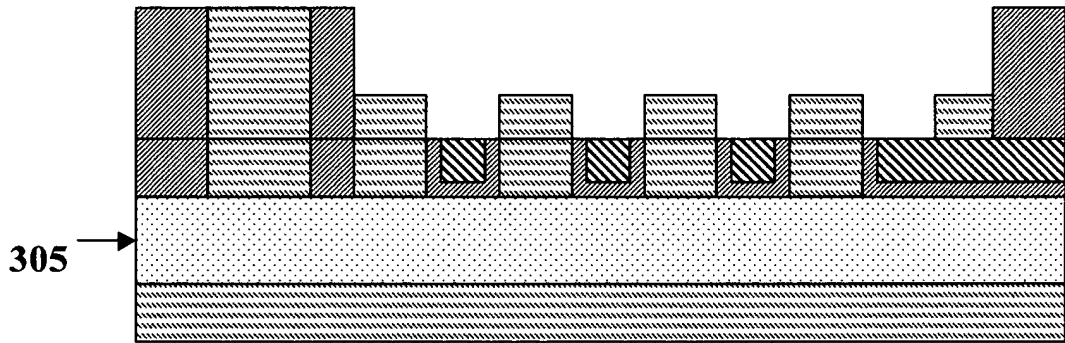


FIG. 9b

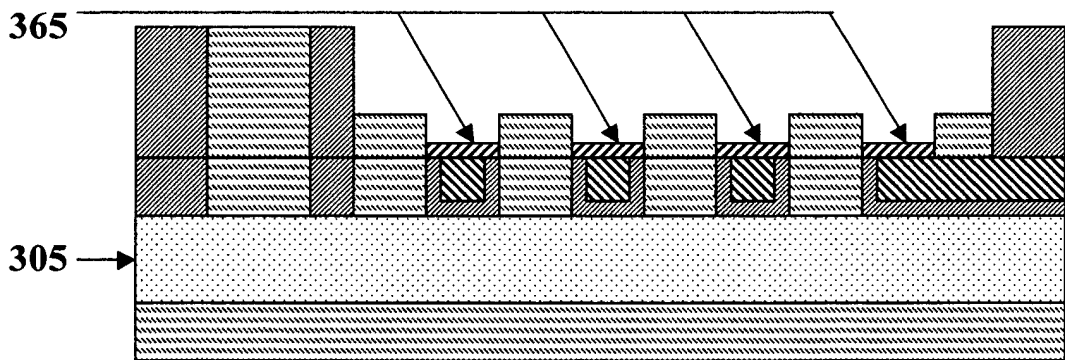


FIG. 9c

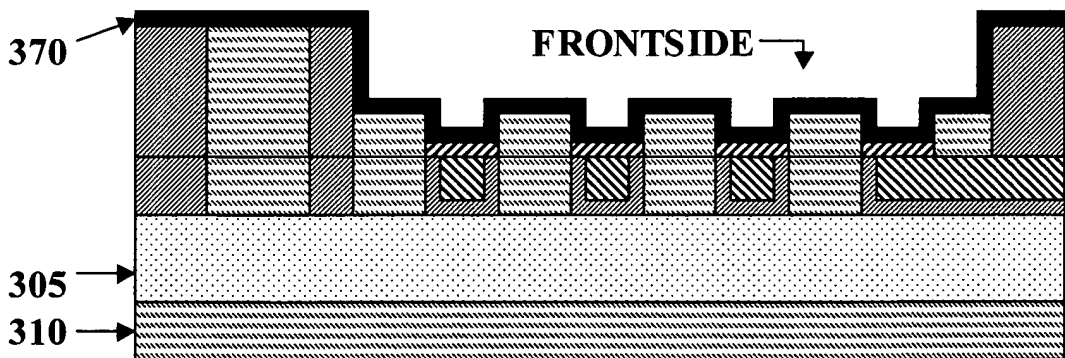
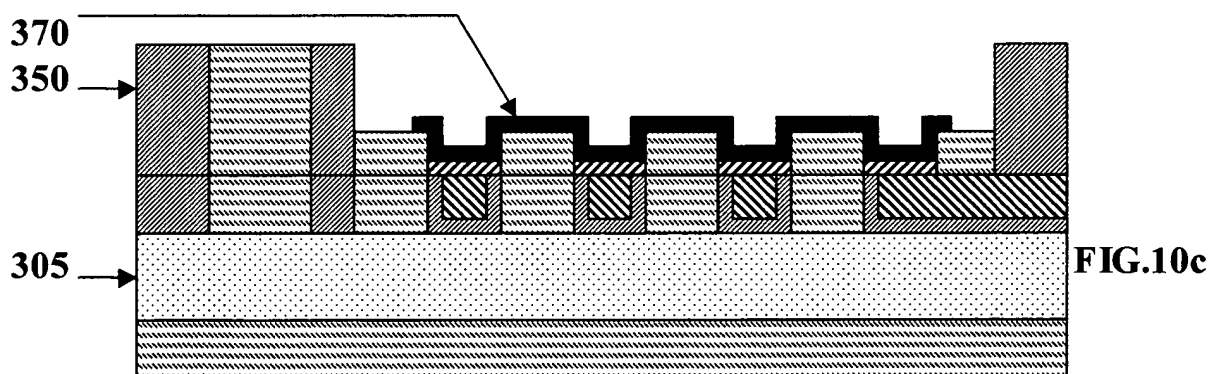
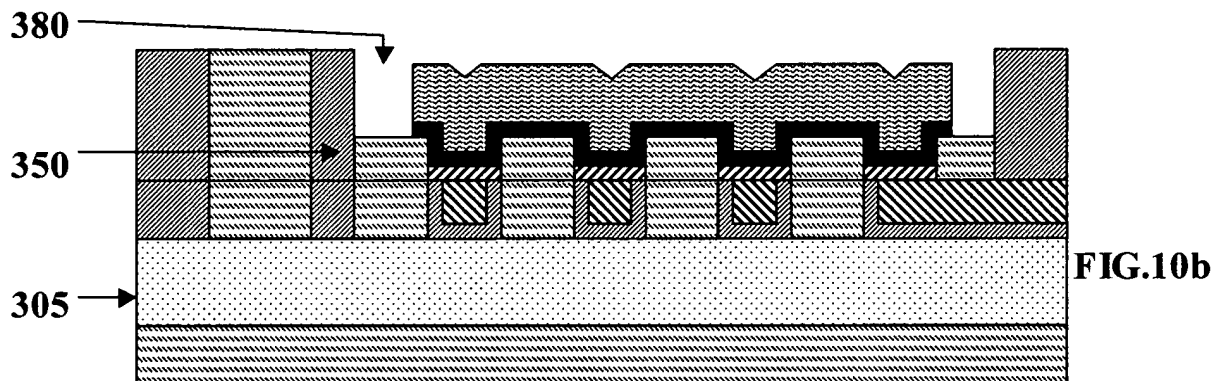
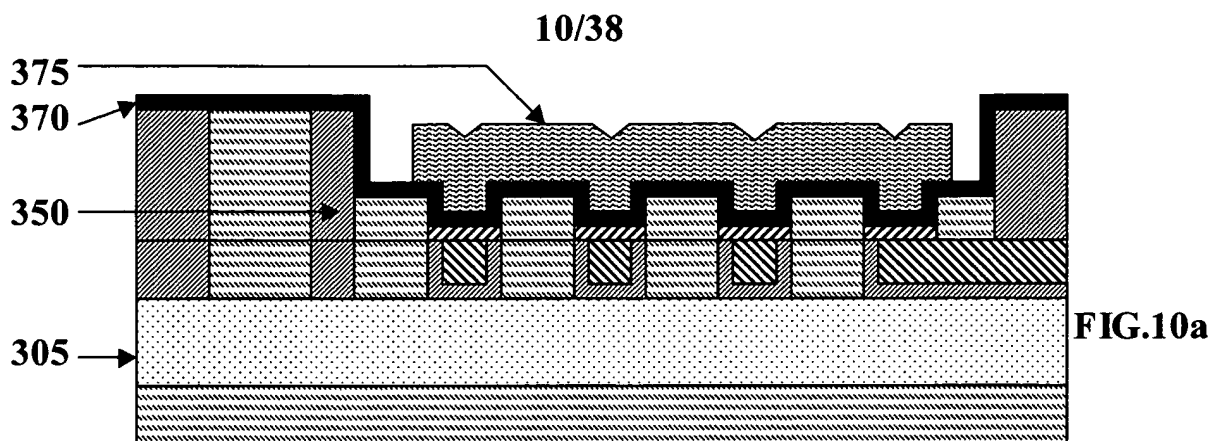


FIG. 9d



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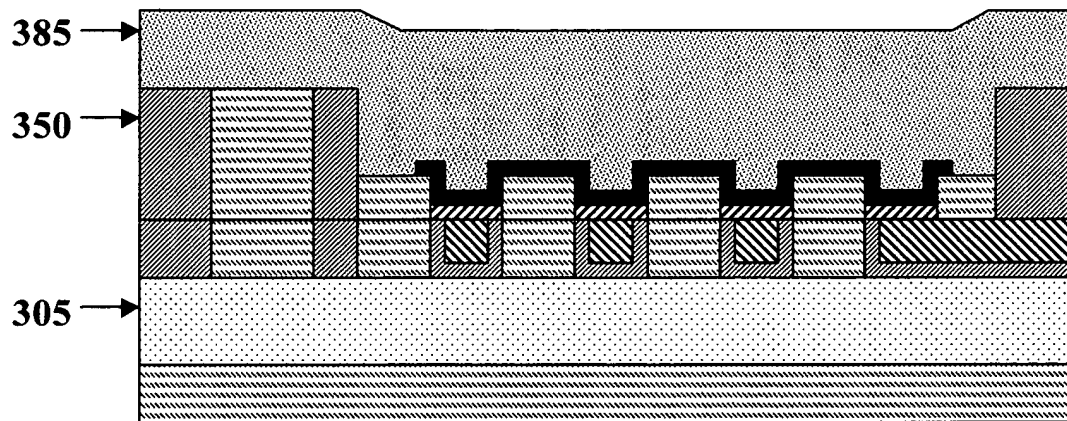


FIG. 11a

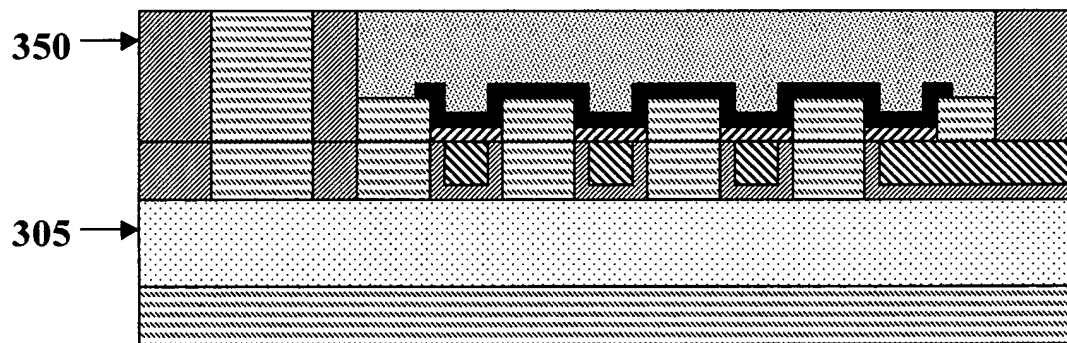


FIG. 11b

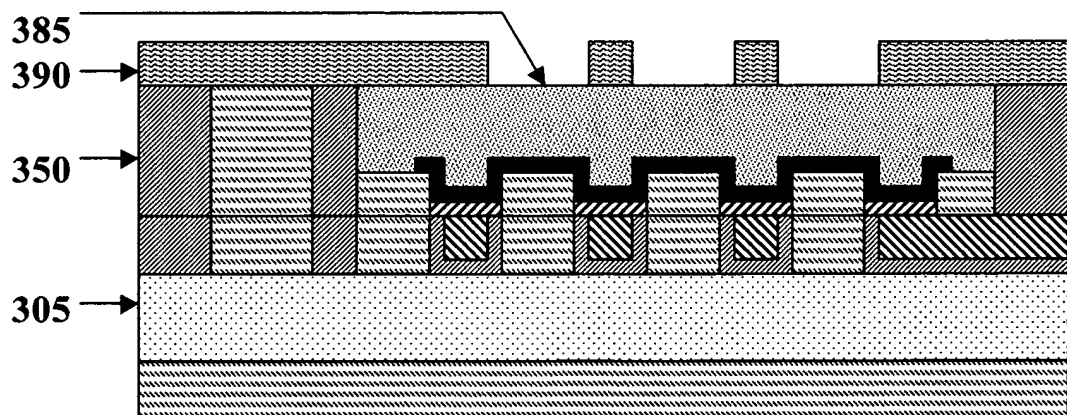


FIG. 11c

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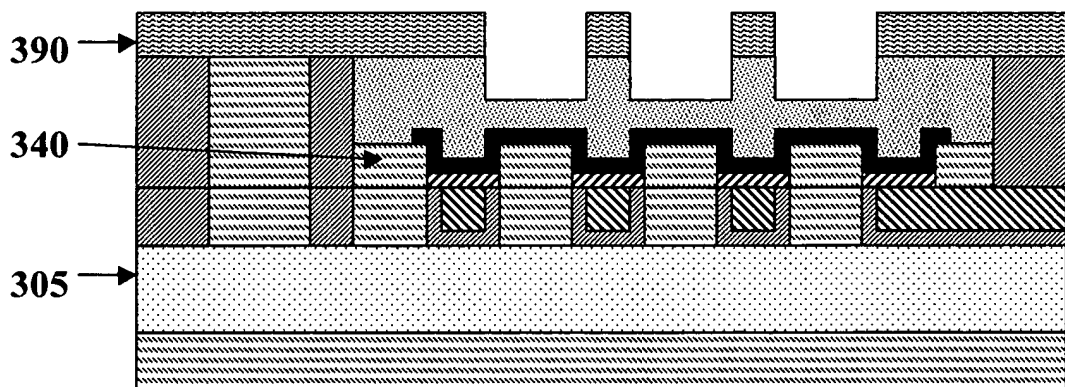


FIG. 12a

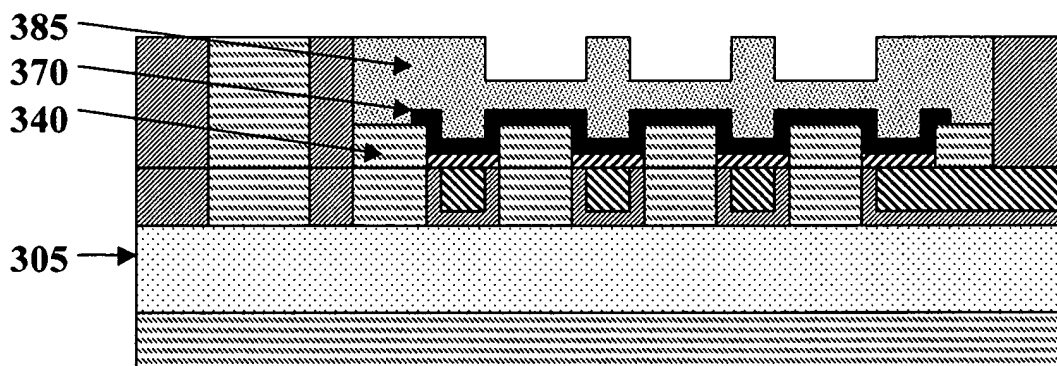


FIG. 12b

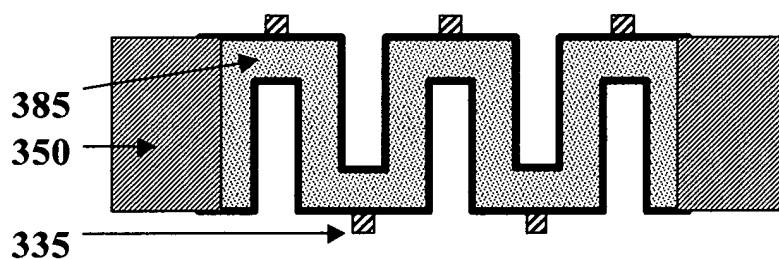


FIG. 12c

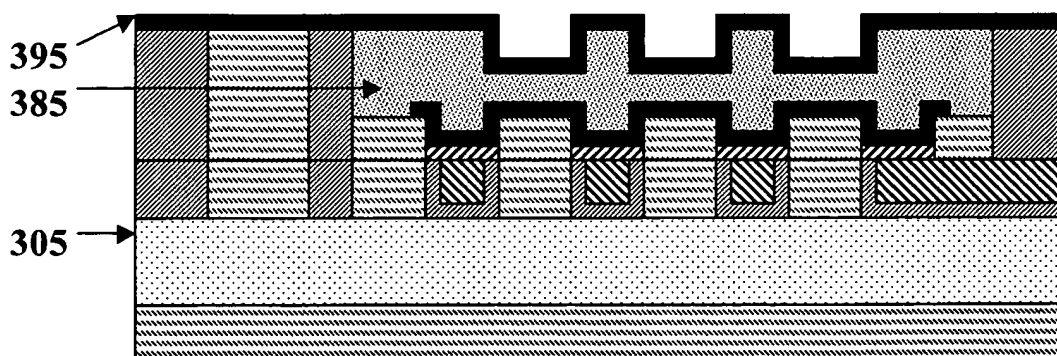


FIG. 12d

This cross-sectional view shows a semiconductor device. A top layer, labeled 400, has a wavy, undulating surface. Below this is a layer labeled 395, which contains a series of rectangular openings or recesses. The bottom of the device is a base layer labeled 305, which has a uniform, fine-grained texture. The device is shown in a cross-section, revealing the internal structure and the relationship between the different layers.

A cross-sectional view of a semiconductor device. The device features a substrate 305 at the bottom, which is a thick layer with a stippled pattern. Above the substrate is a patterned layer 395, which consists of a series of rectangular blocks of different materials (indicated by various hatching patterns: diagonal lines, cross-hatching, and stippling) separated by recessed regions. The top surface of the device is defined by the upper edges of these blocks, forming a stepped profile.

**FIG.13c**

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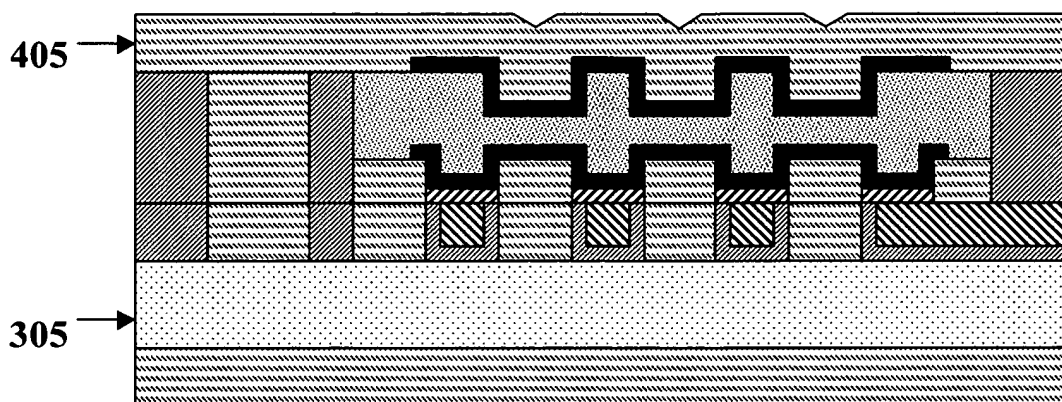


FIG. 14a

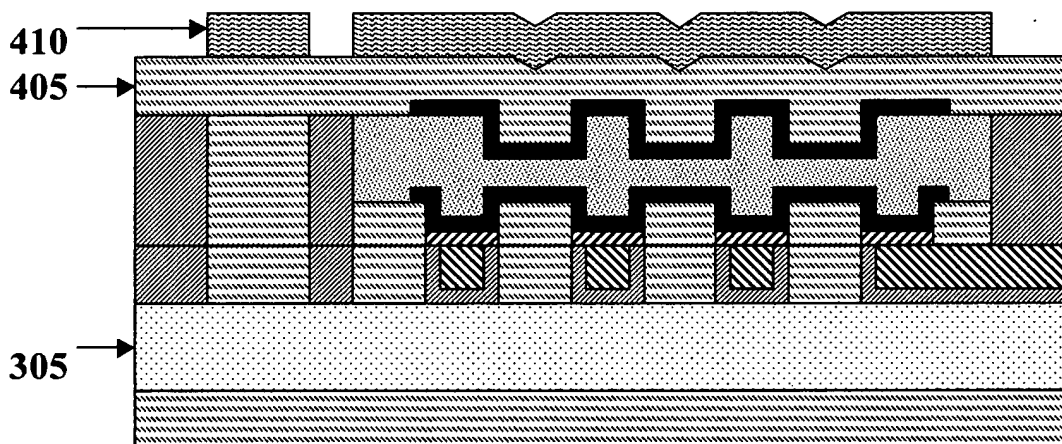


FIG. 14b

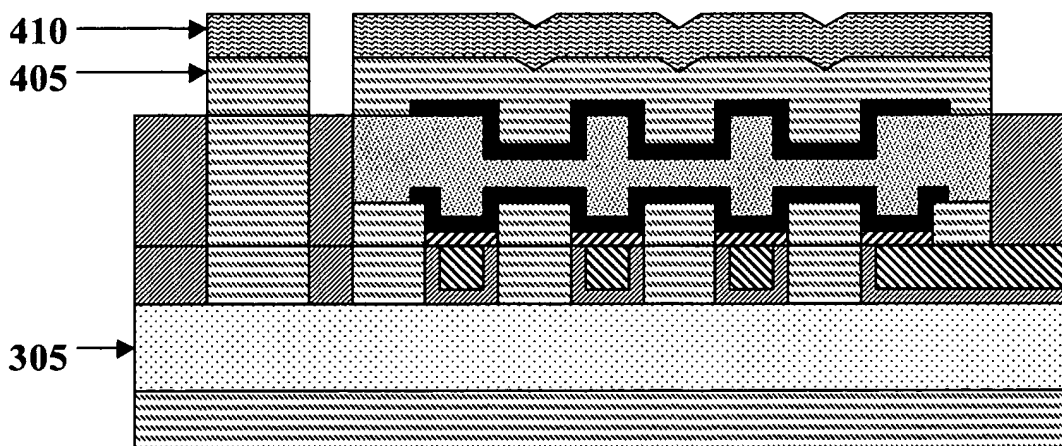
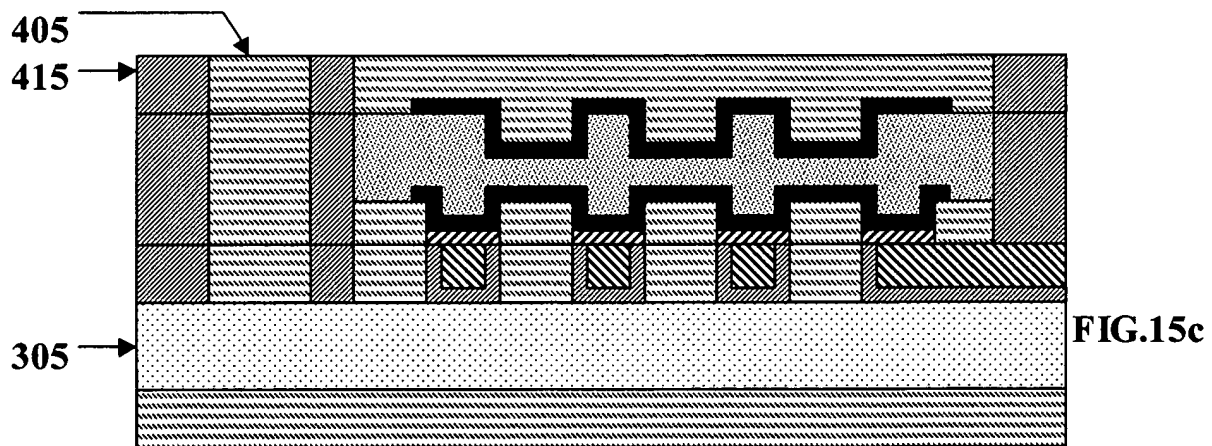
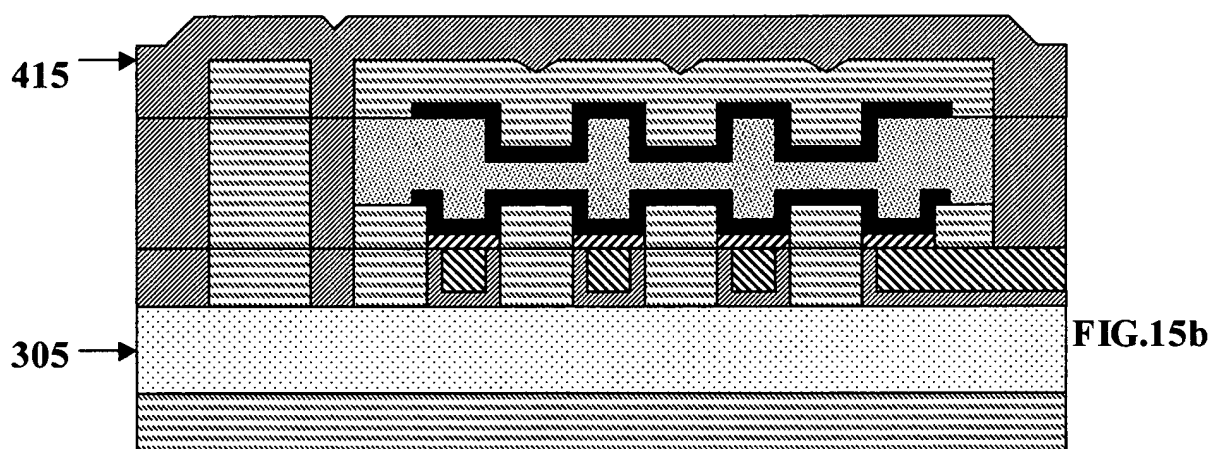
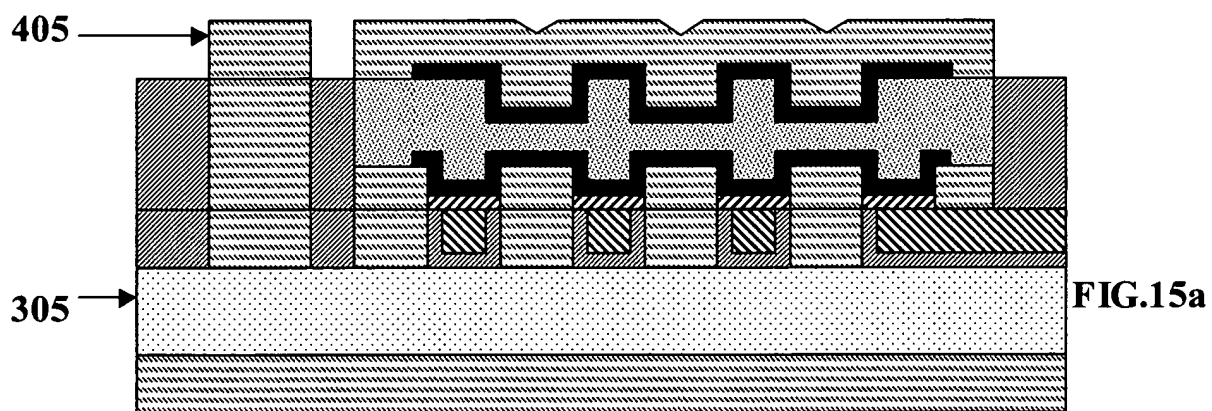


FIG. 14c

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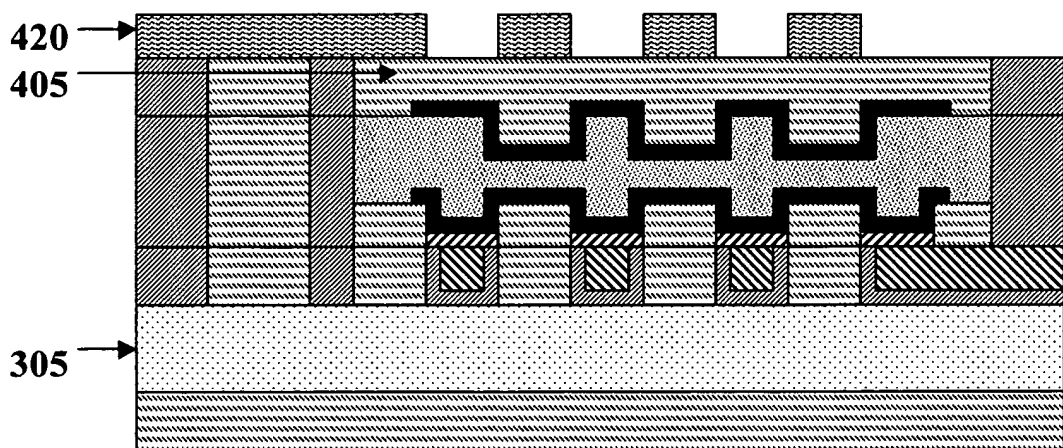


FIG. 16a

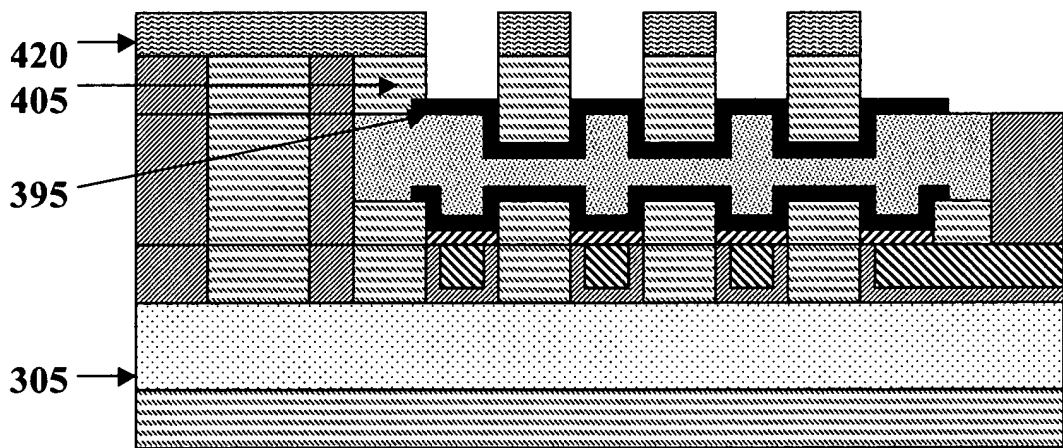


FIG. 16b

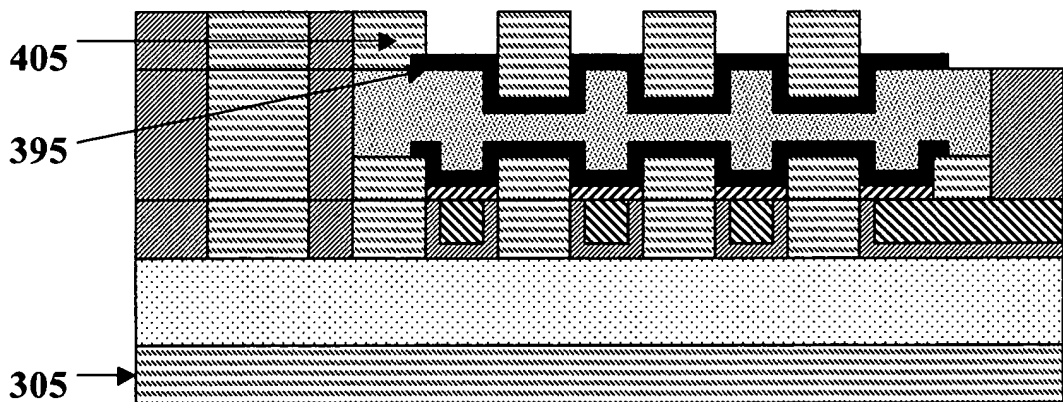
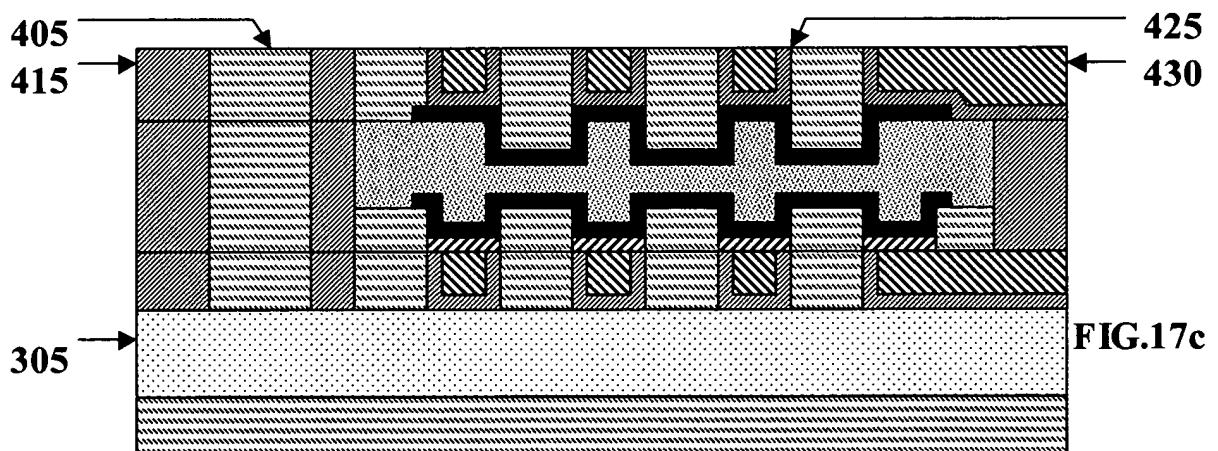
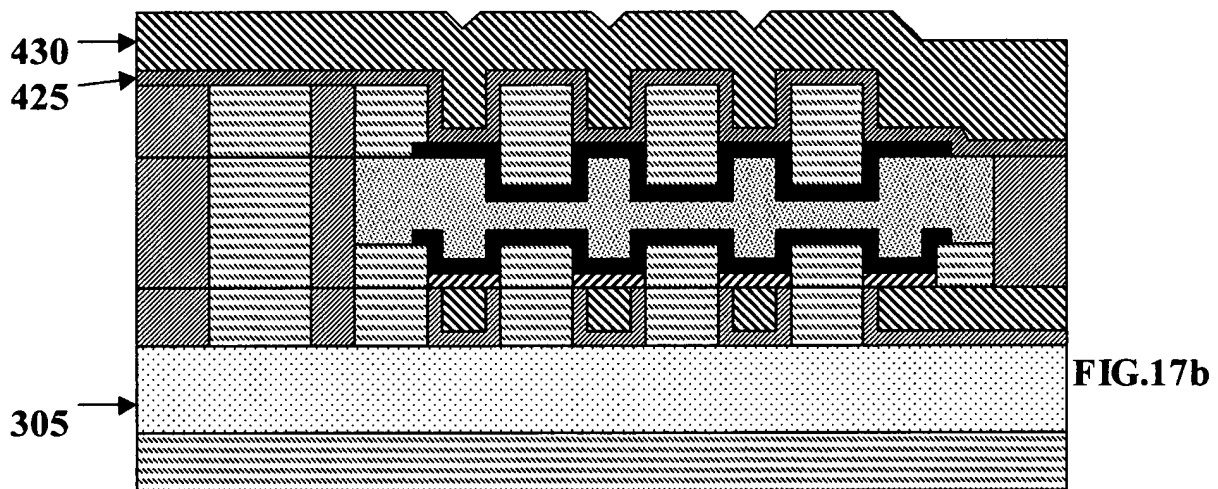
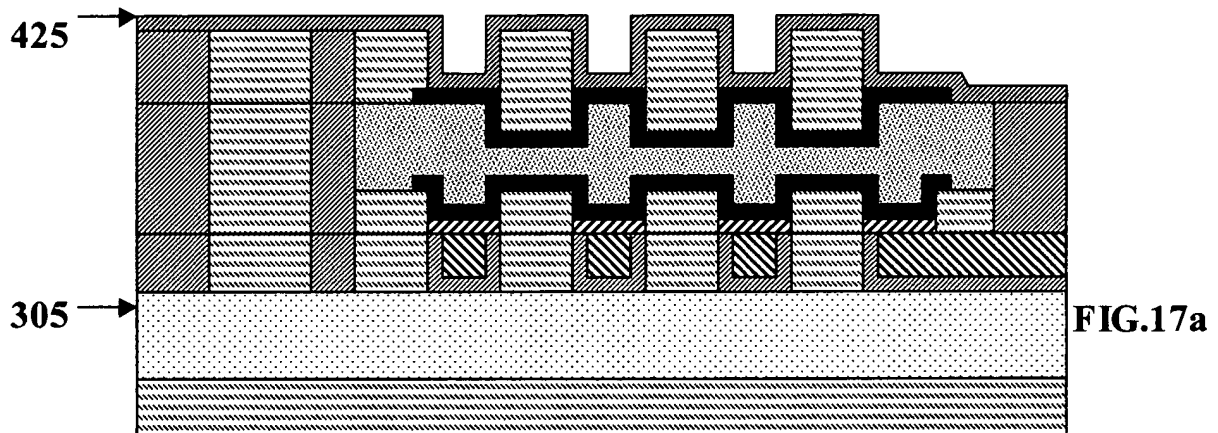


FIG. 16c



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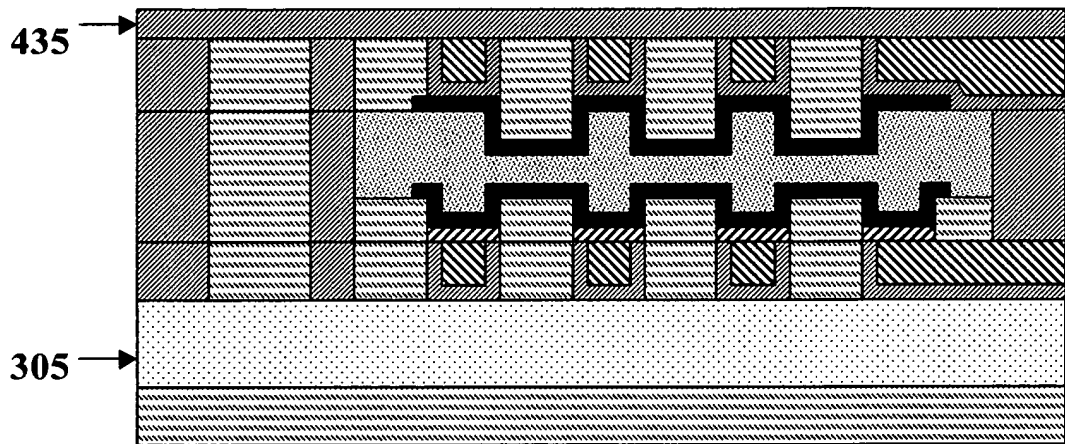


FIG. 18a

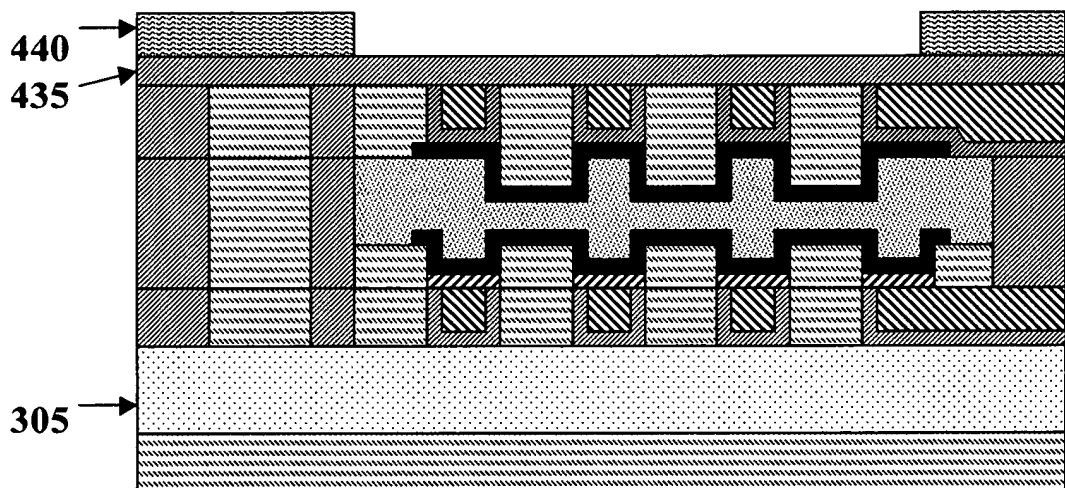


FIG. 18b

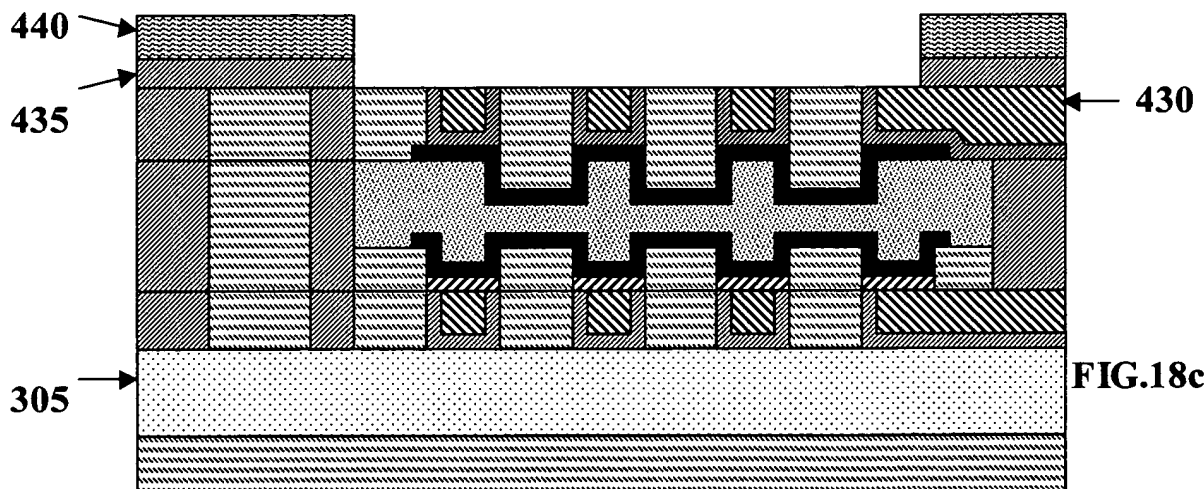
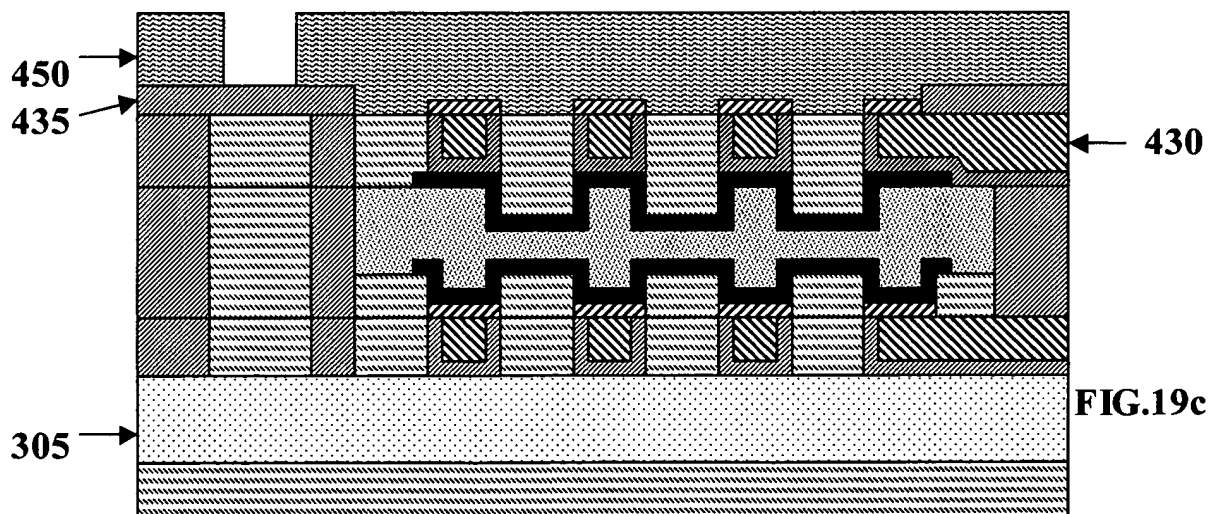
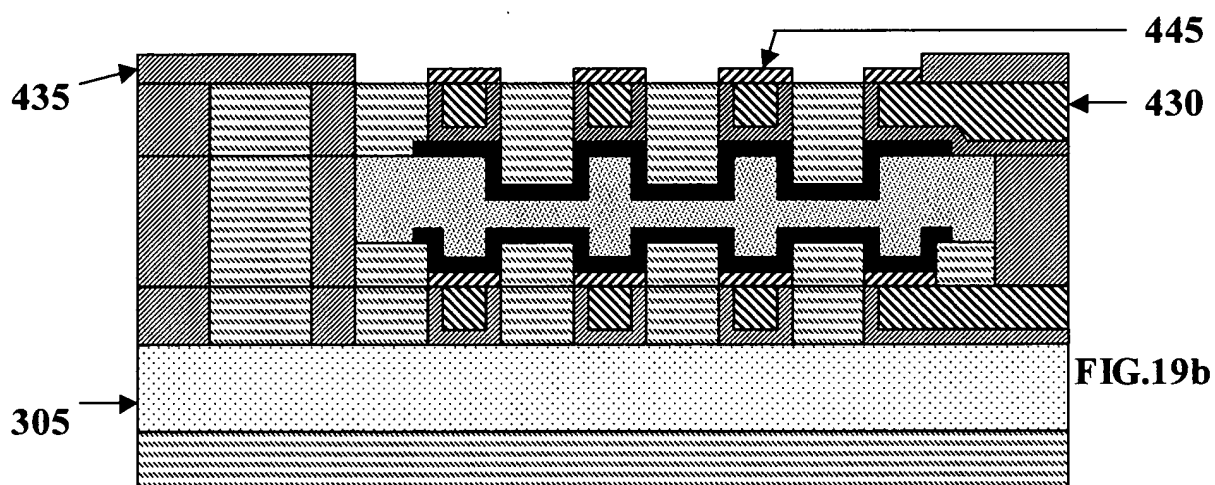
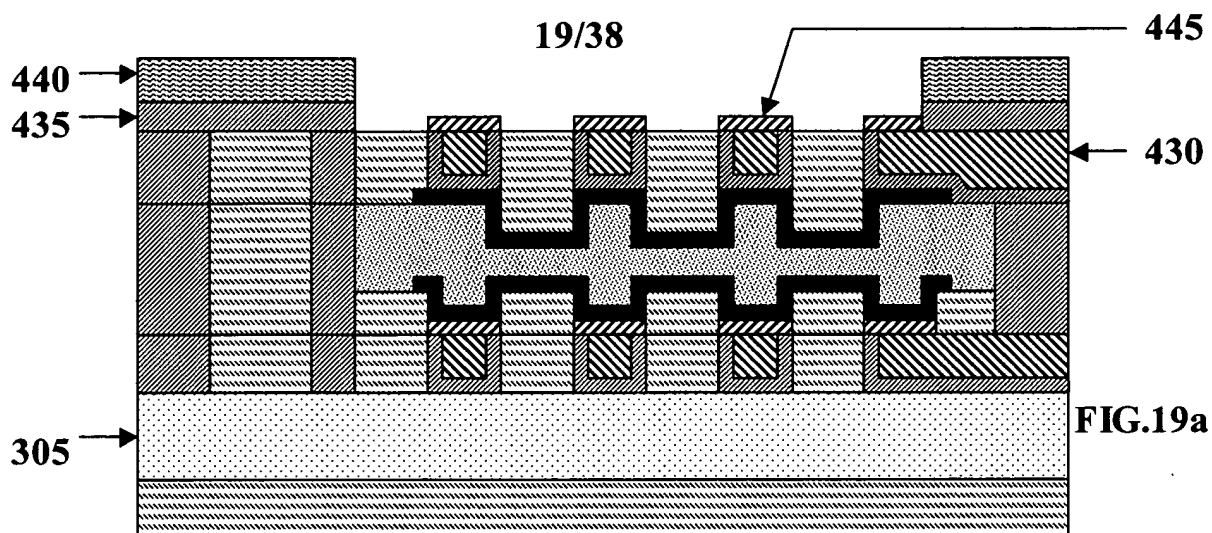


FIG. 18c

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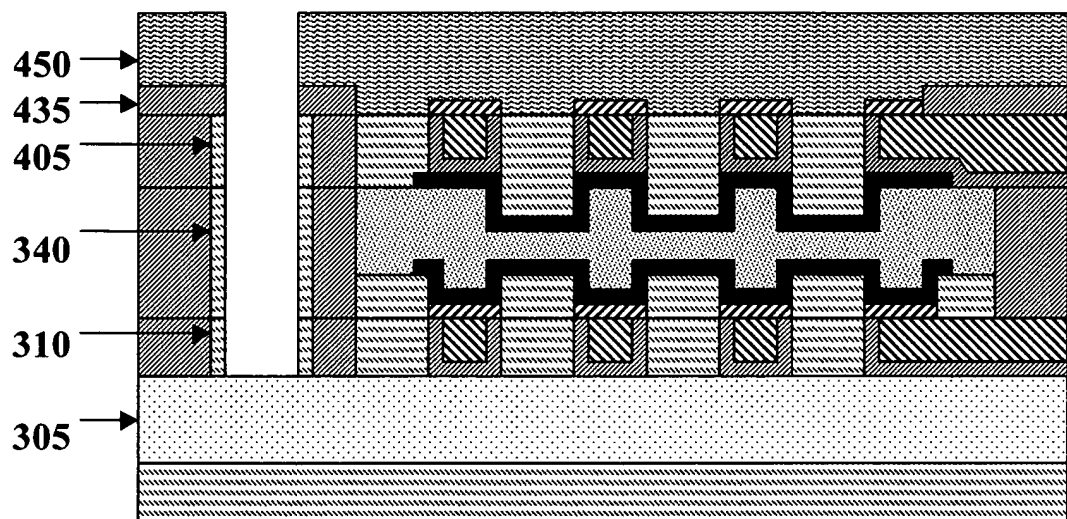


FIG.20a

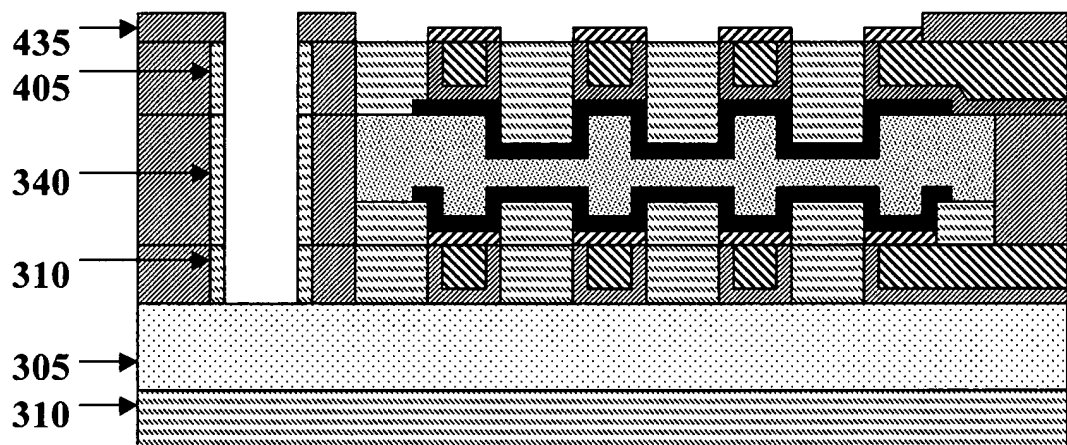
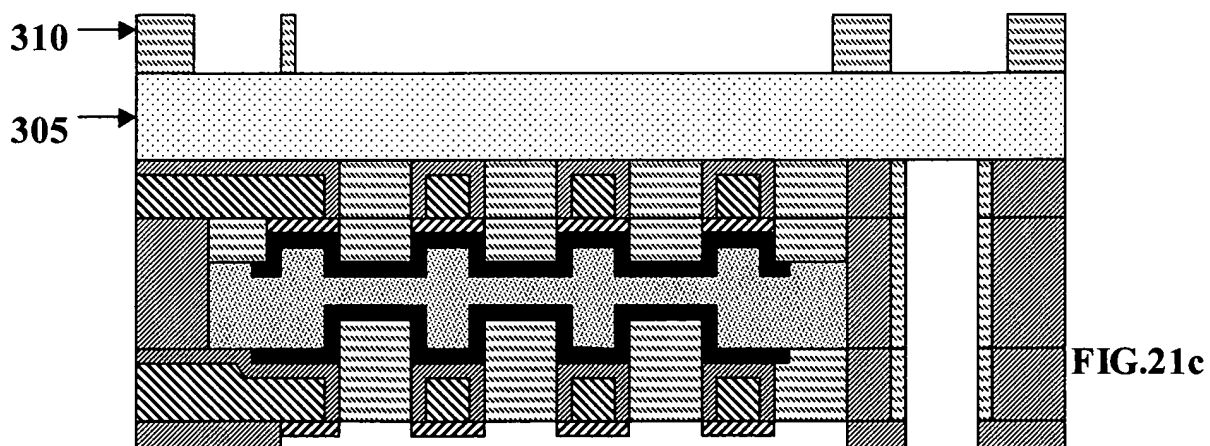
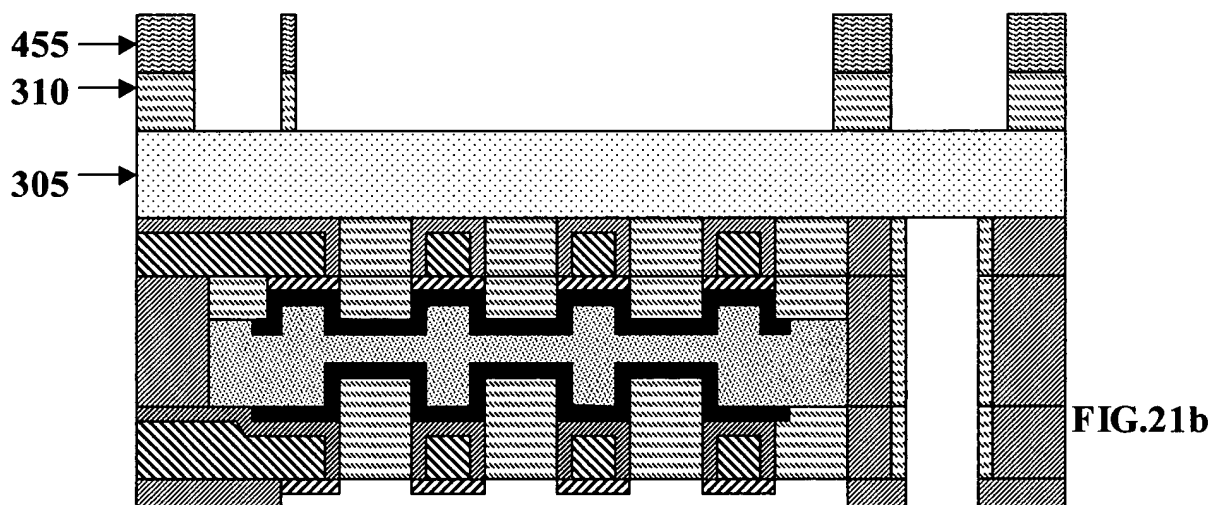
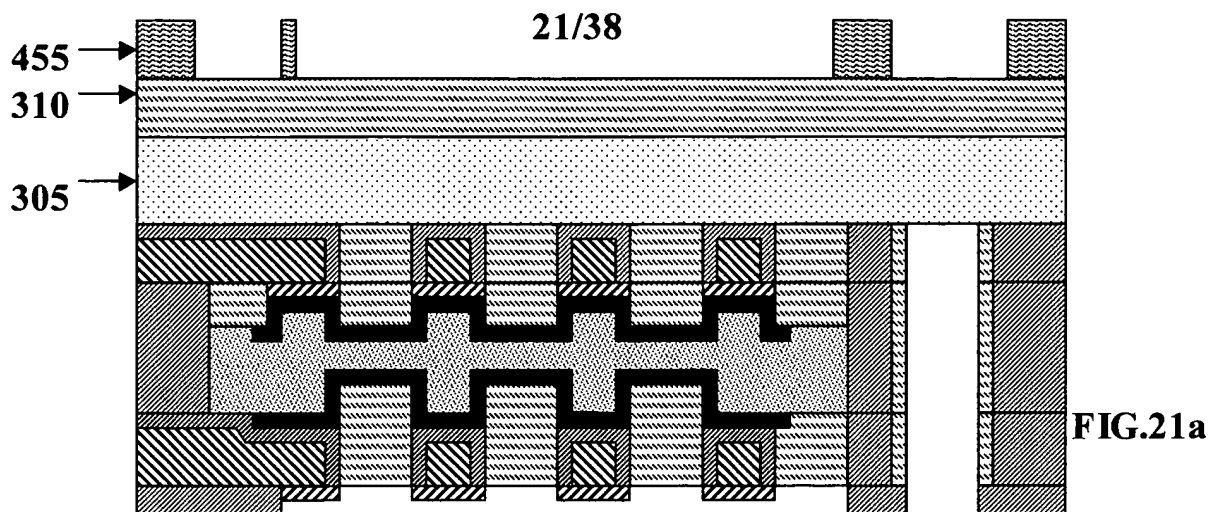
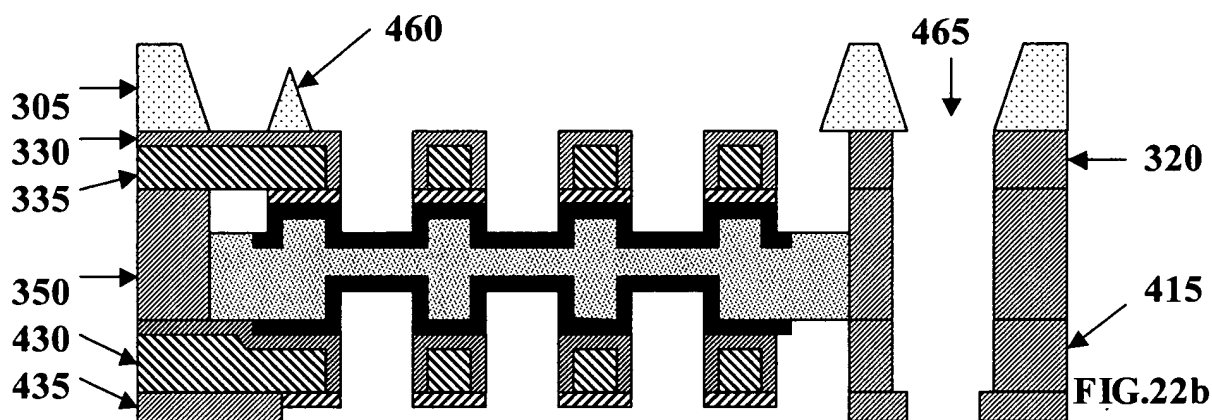
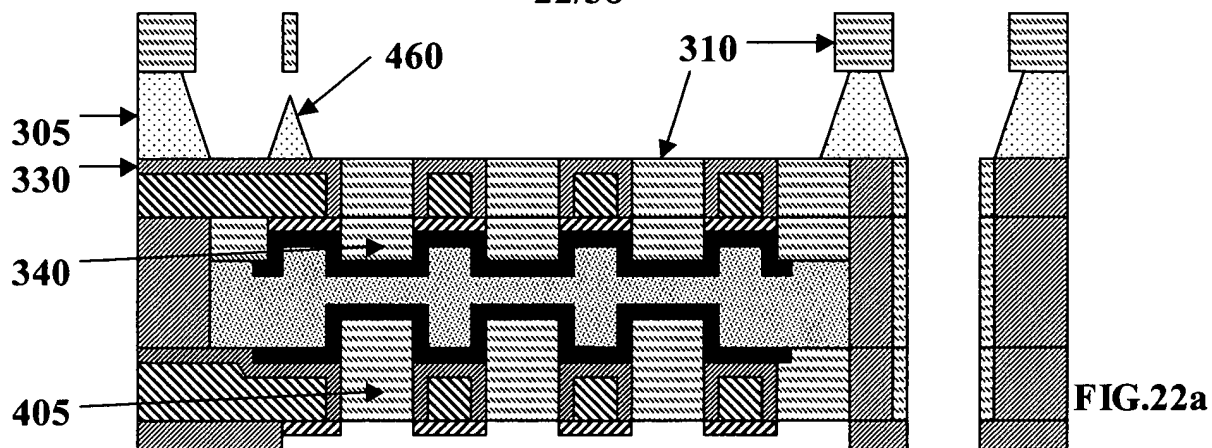


FIG.20b

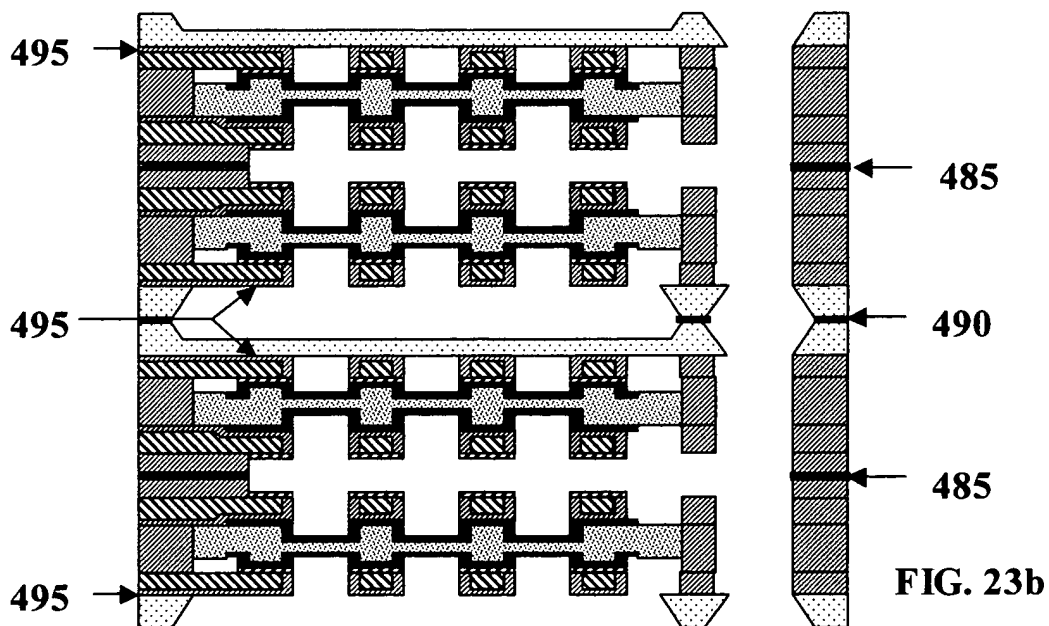
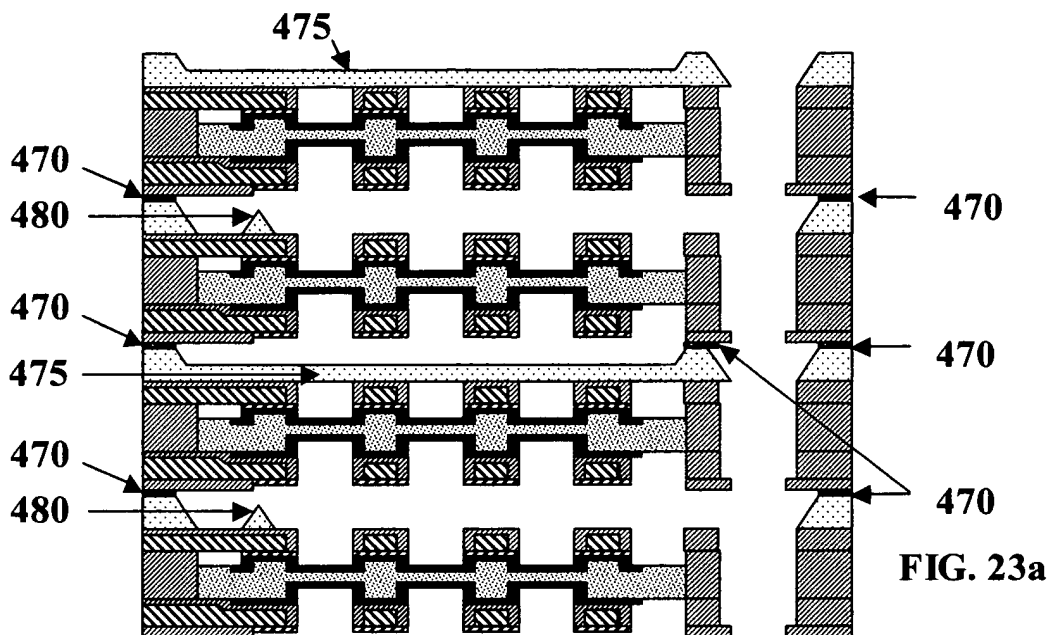
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CONTACT: BURT W. FOWLER (512)262-2700



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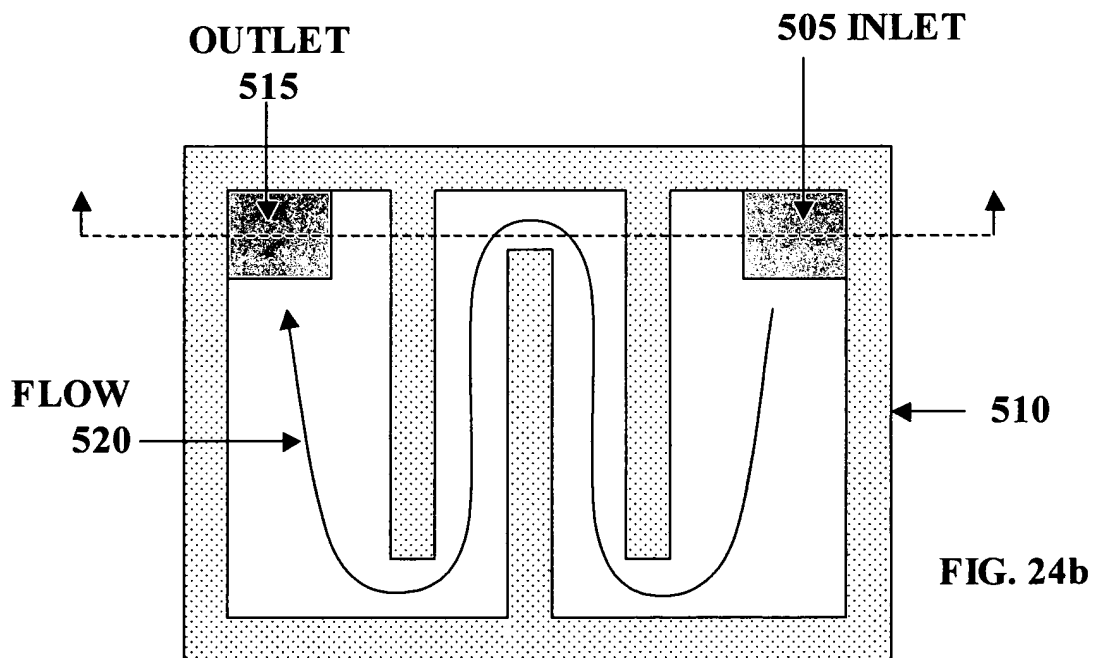
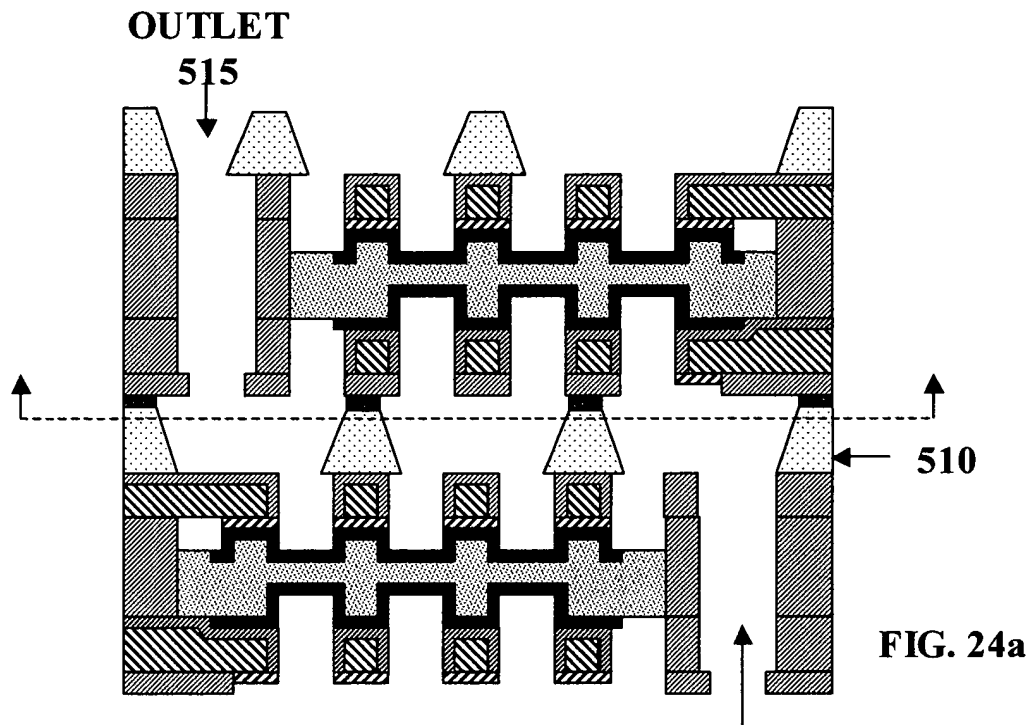


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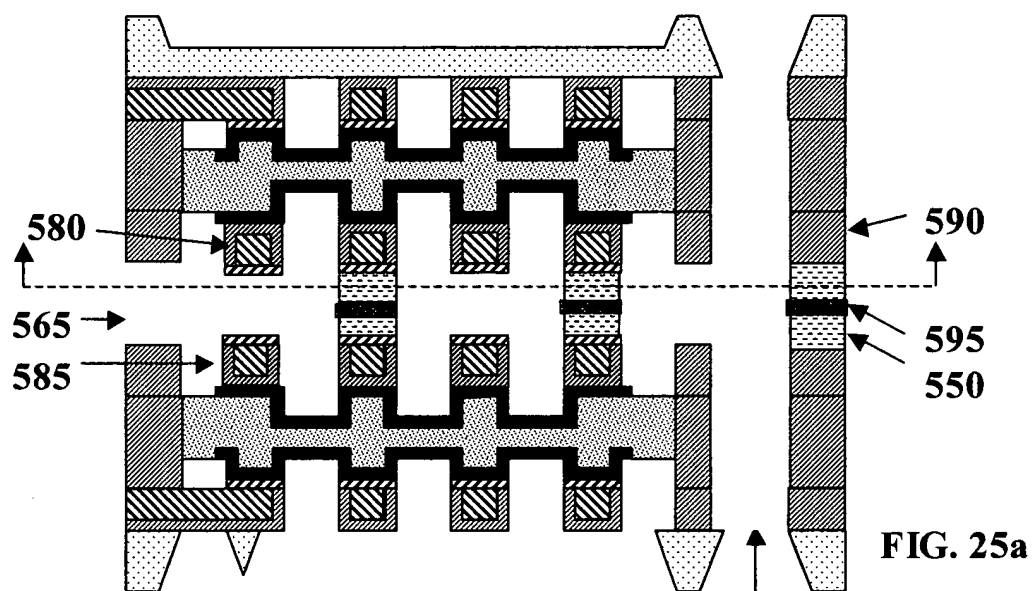


FIG. 25a

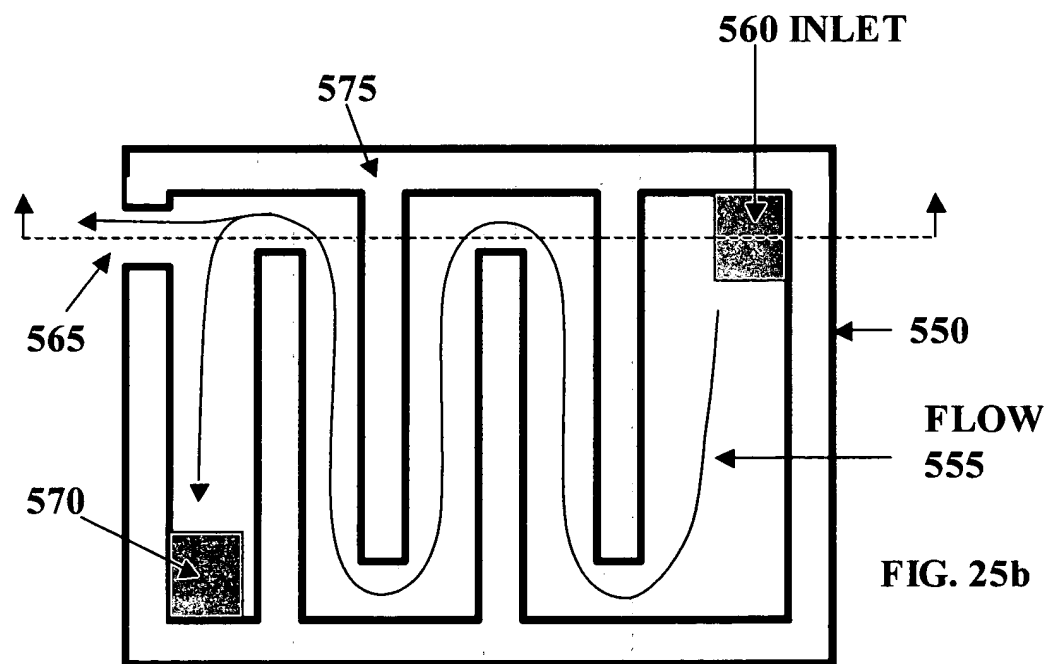
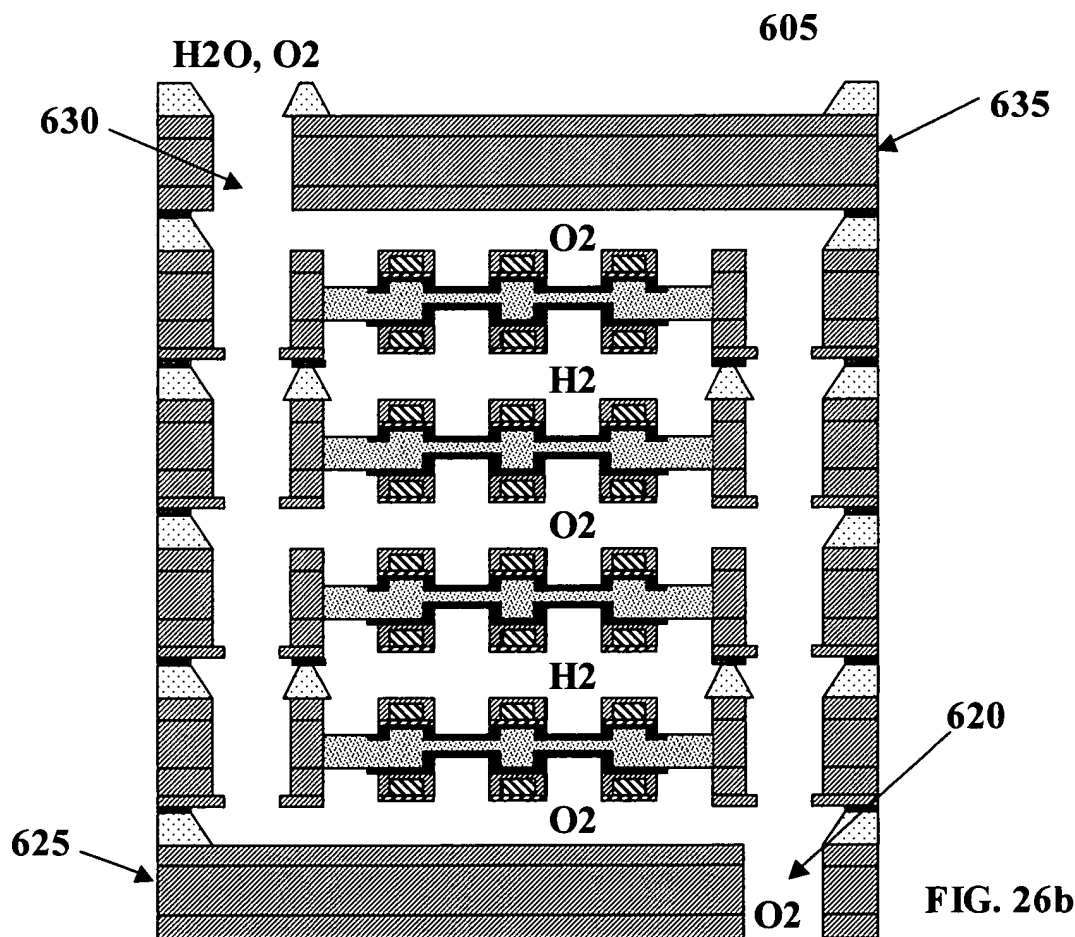
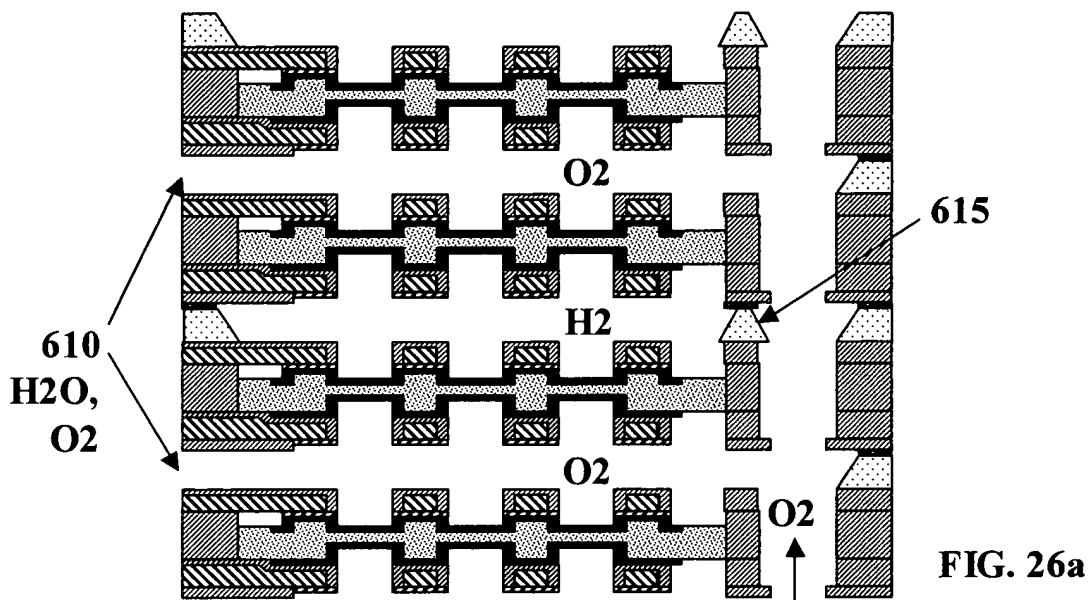


FIG. 25b

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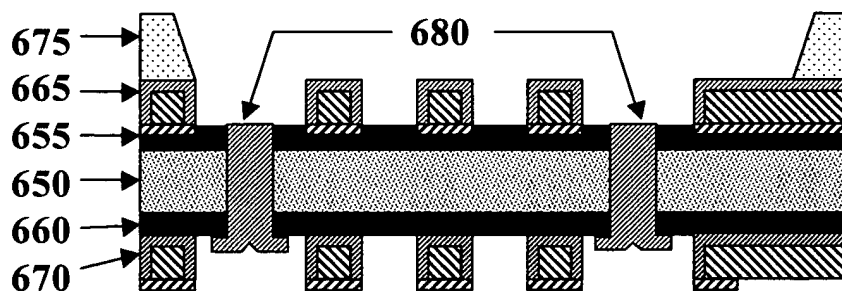


FIG. 27a

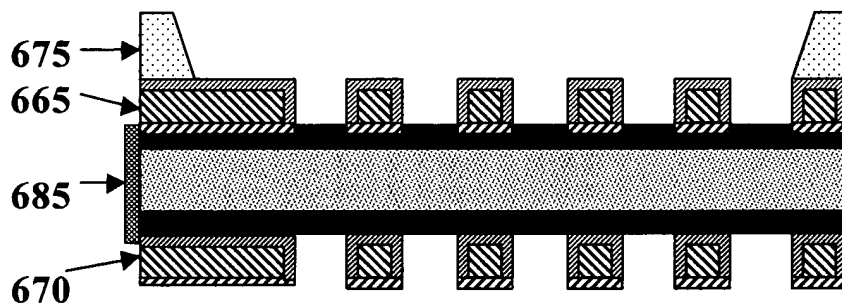


FIG. 27b

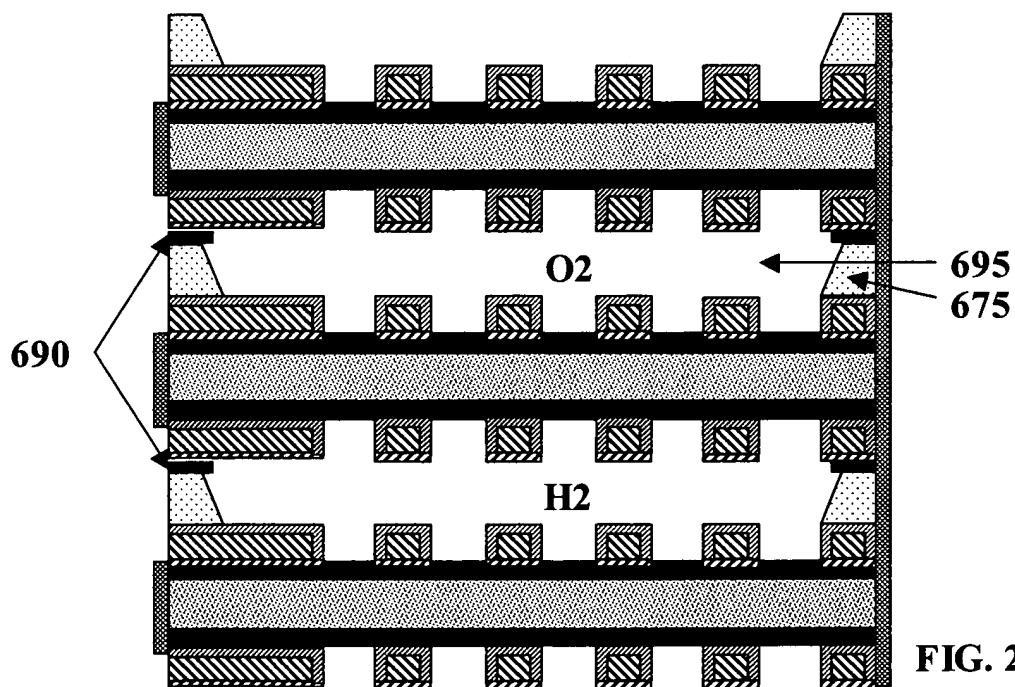


FIG. 27c

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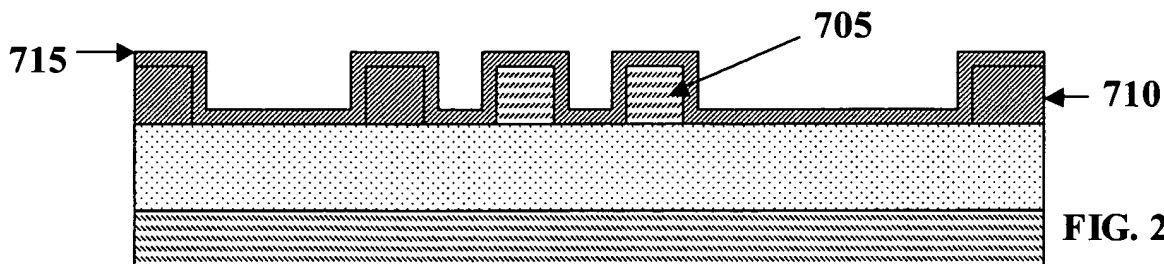


FIG. 28a

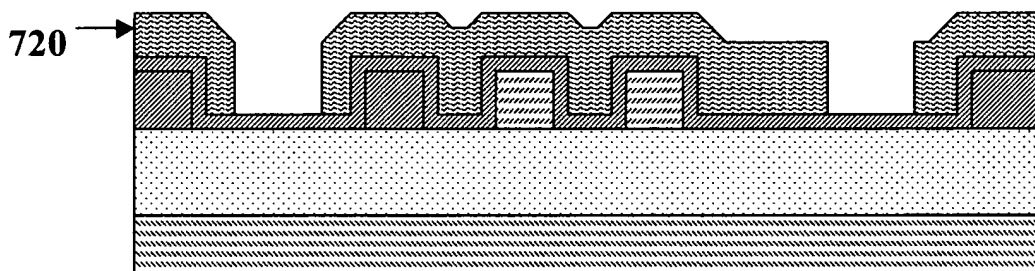


FIG. 28b

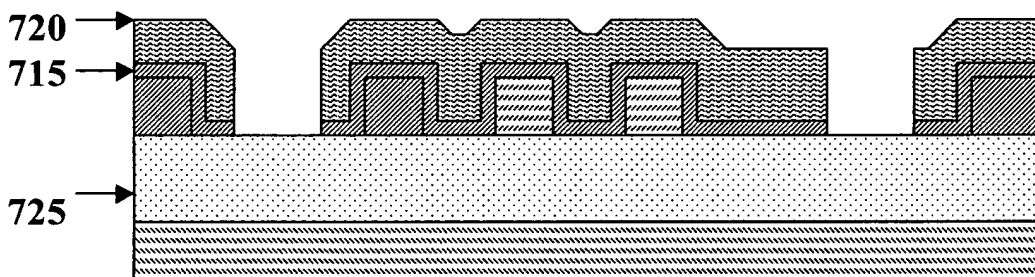


FIG. 28c

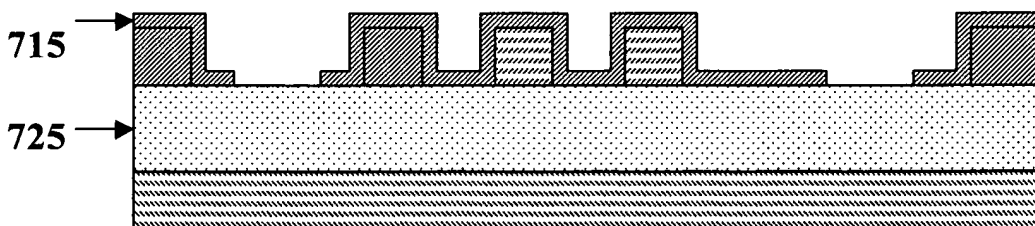


FIG. 28d



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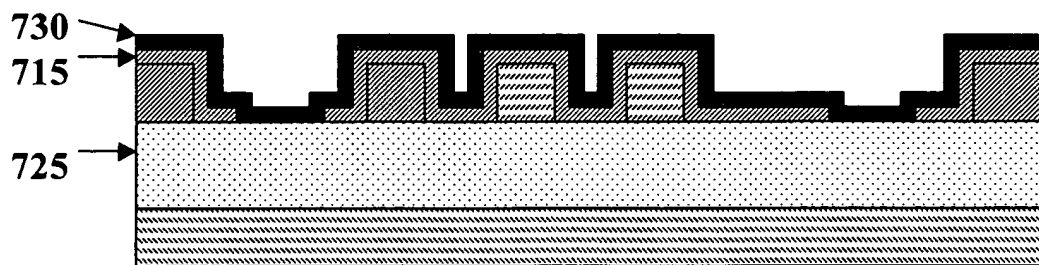


FIG. 29a

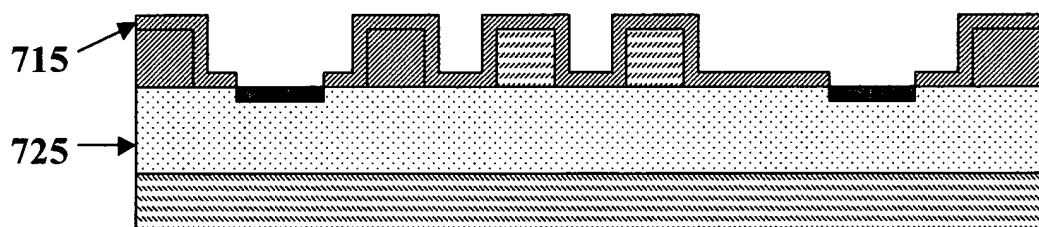


FIG. 29b

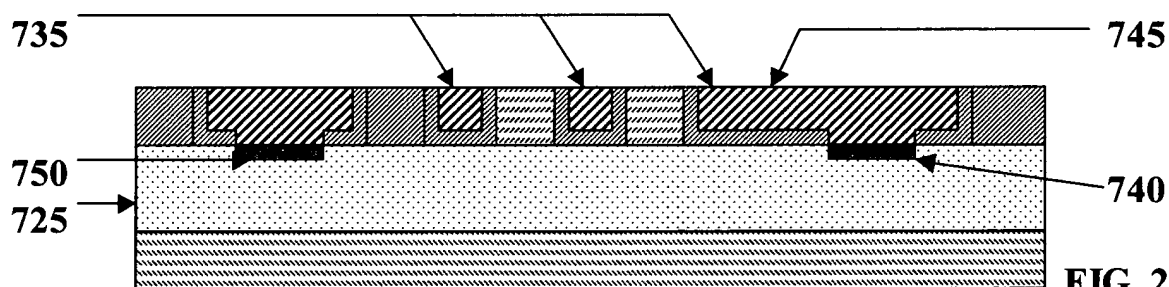


FIG. 29c

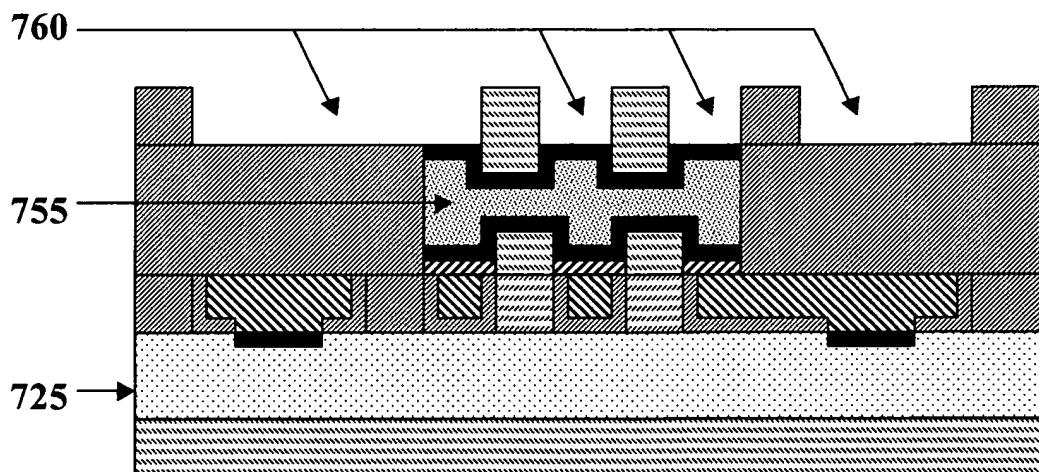
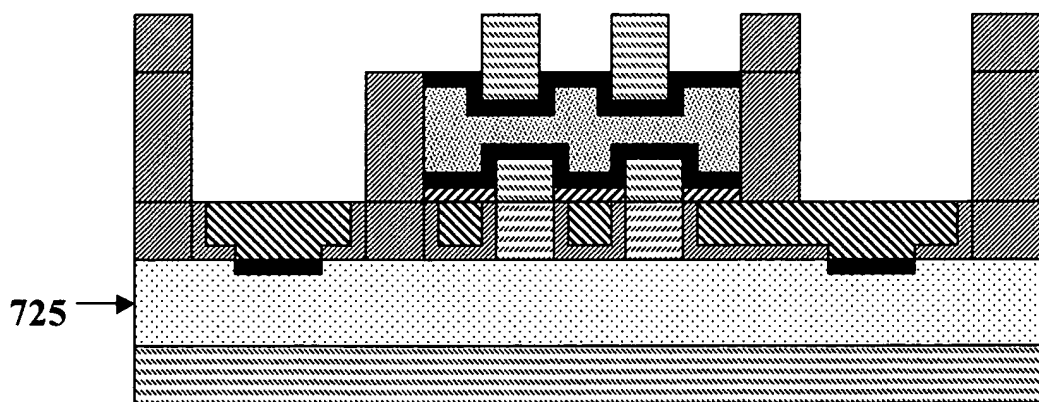
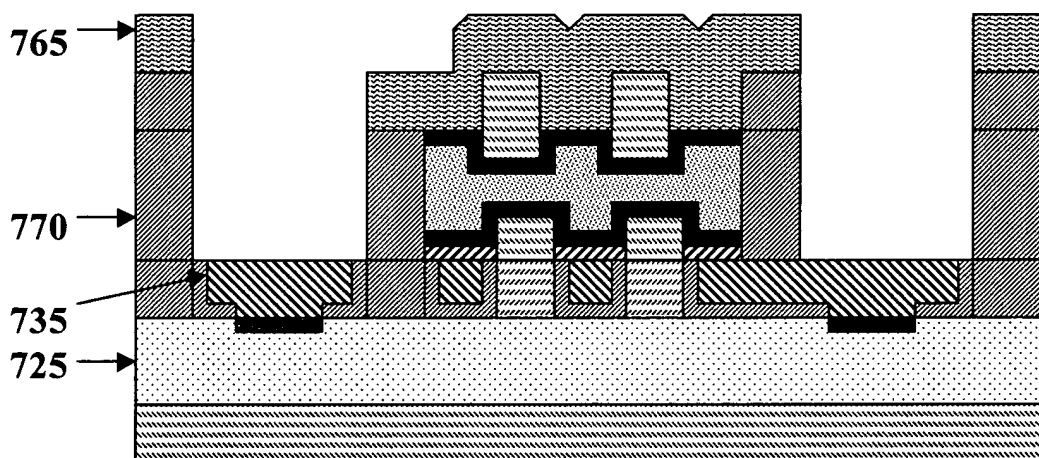
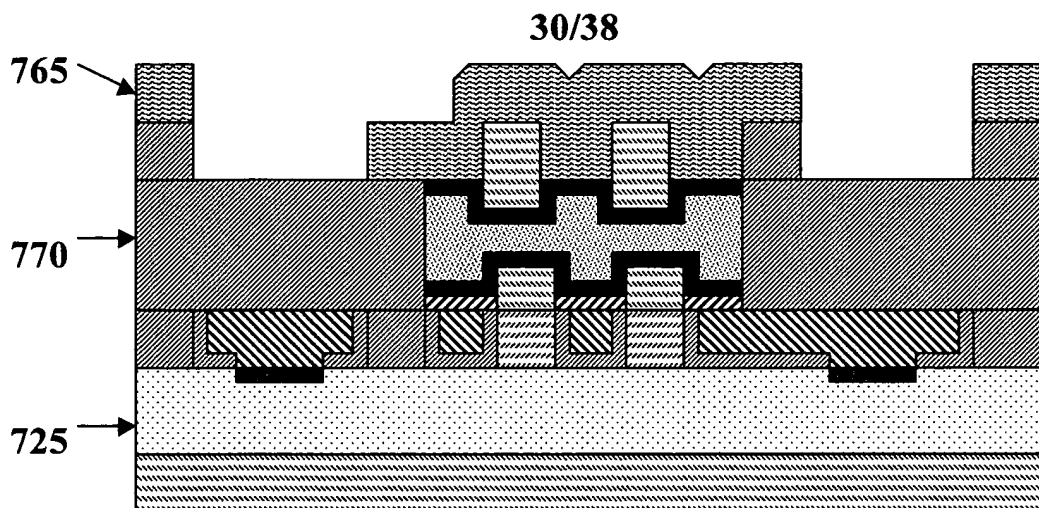


FIG. 29d



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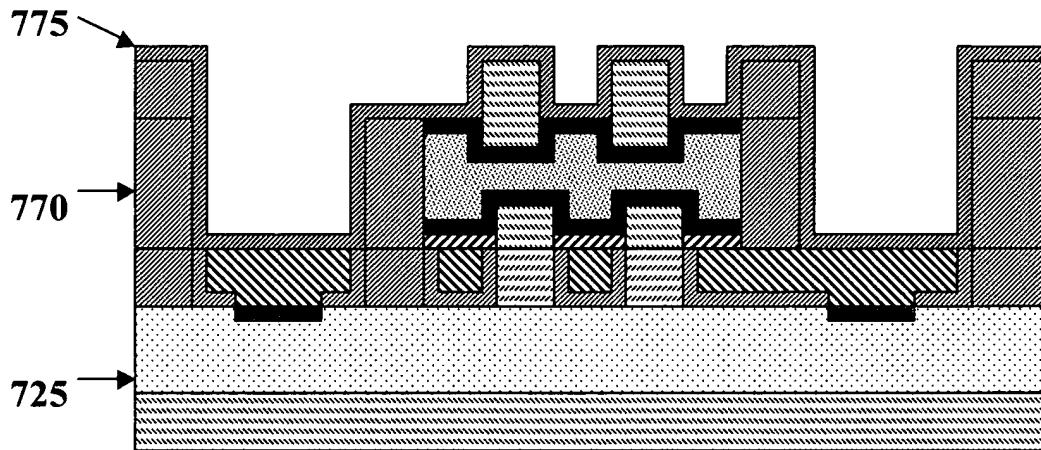


FIG. 31a

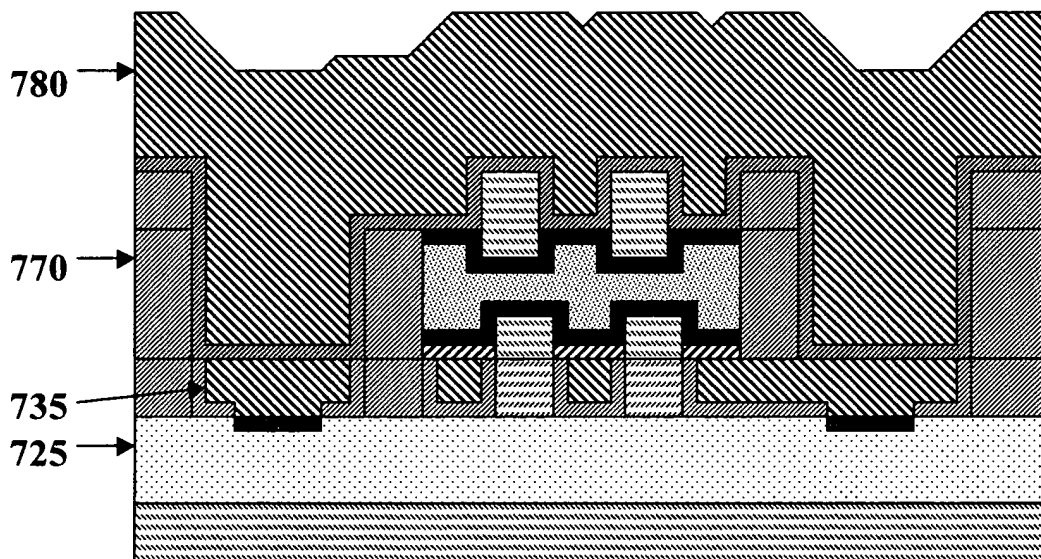


FIG. 31b

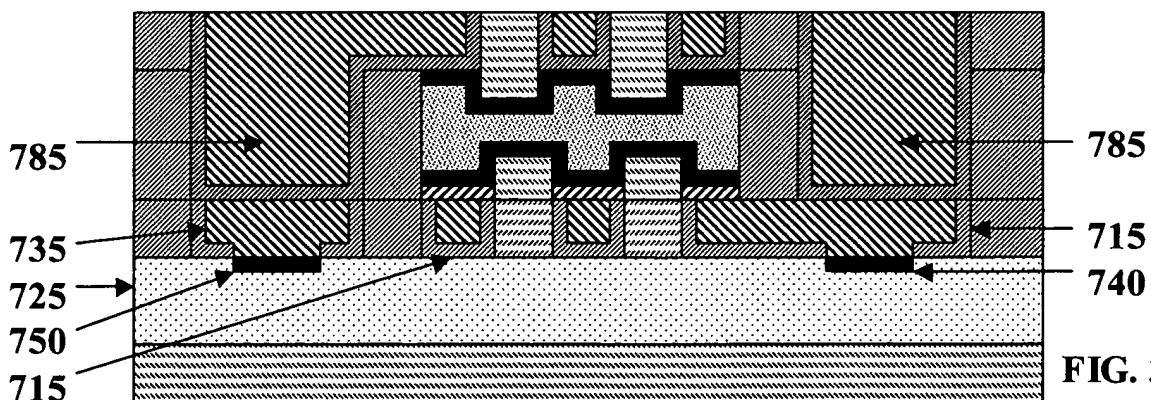
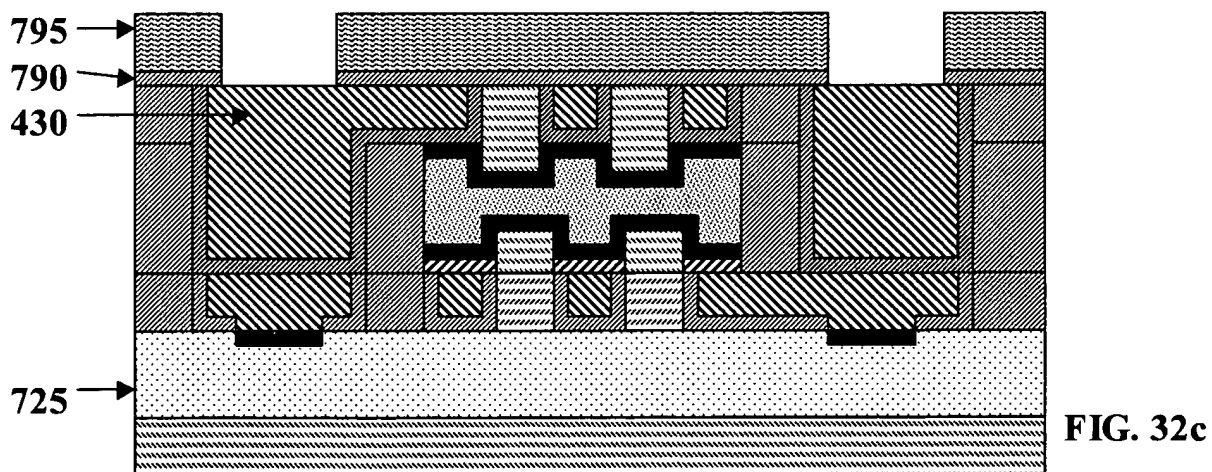
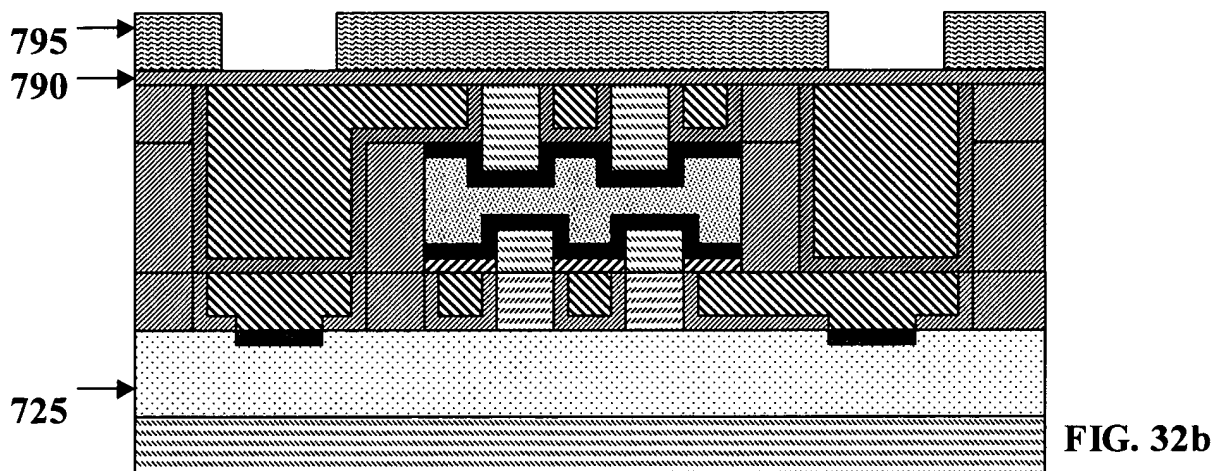
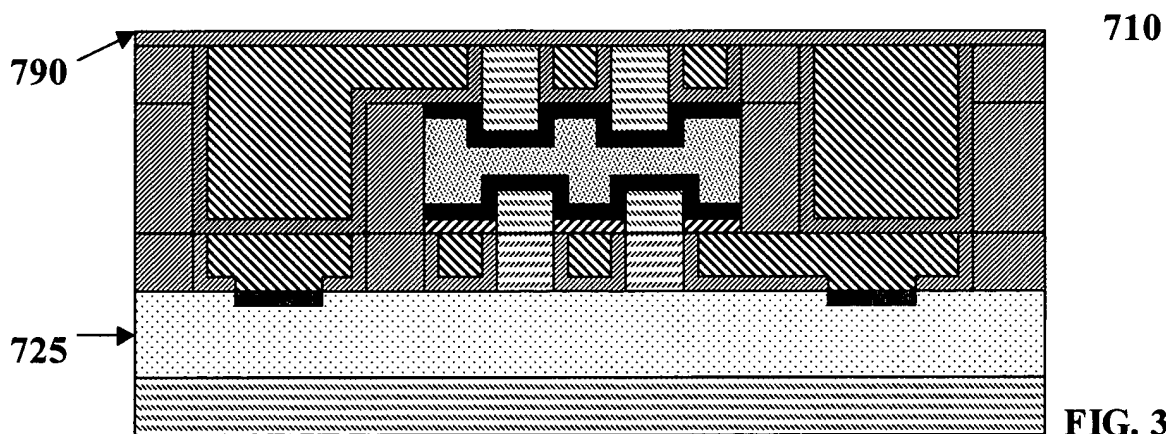


FIG. 31c

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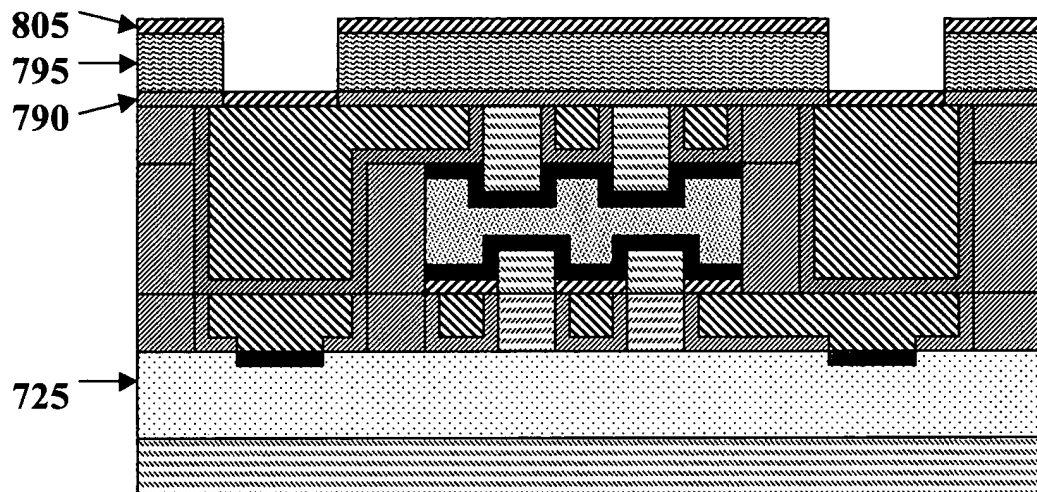


FIG. 33a

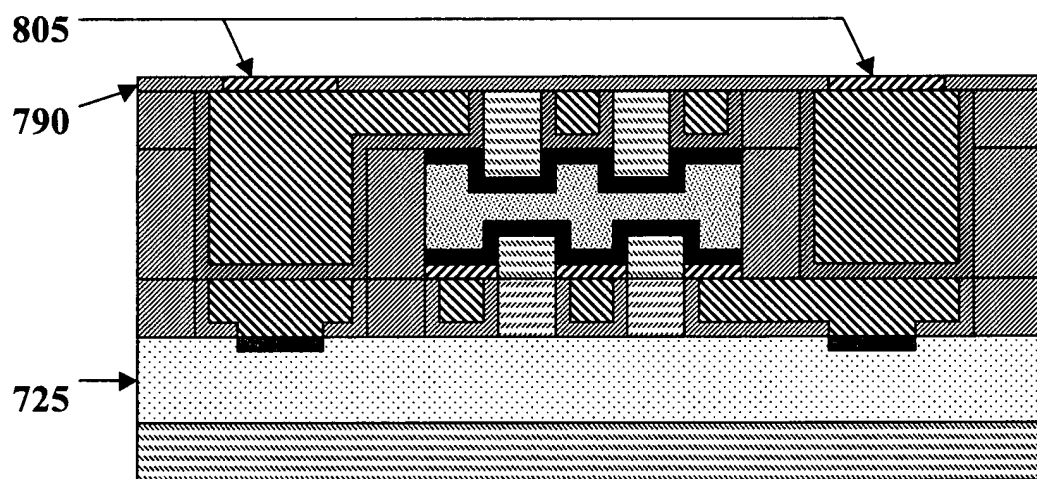


FIG. 33b

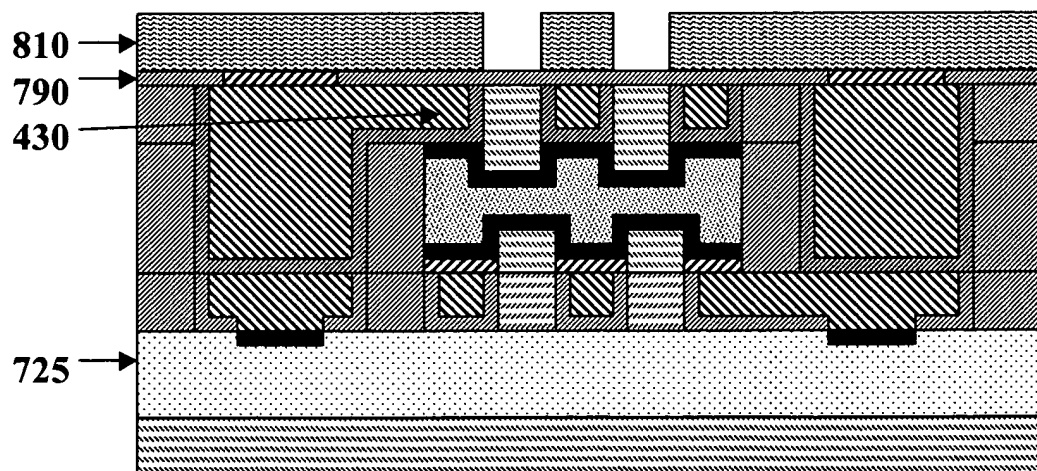


FIG. 33c

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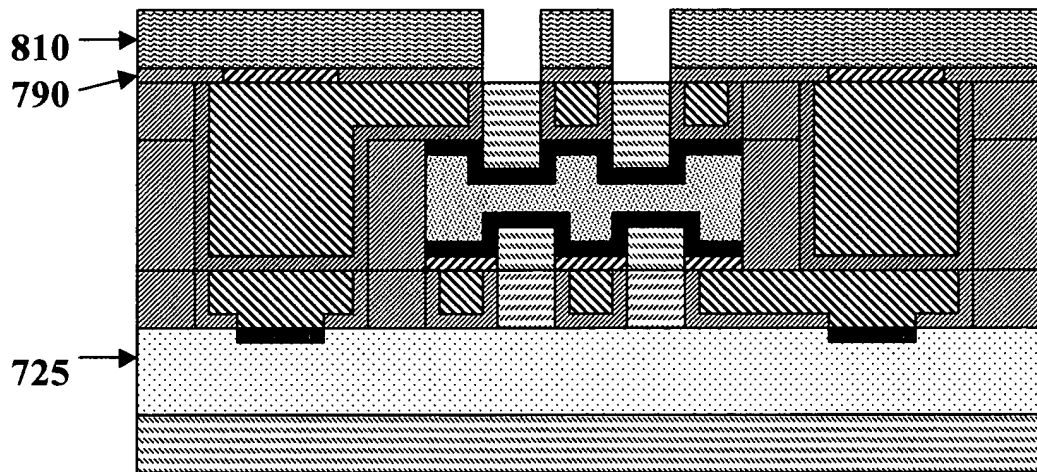


FIG. 34a

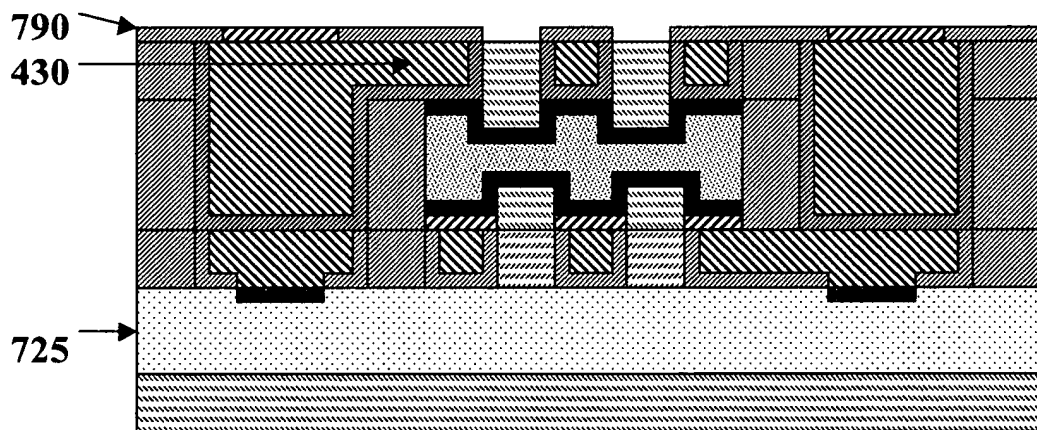


FIG. 34b

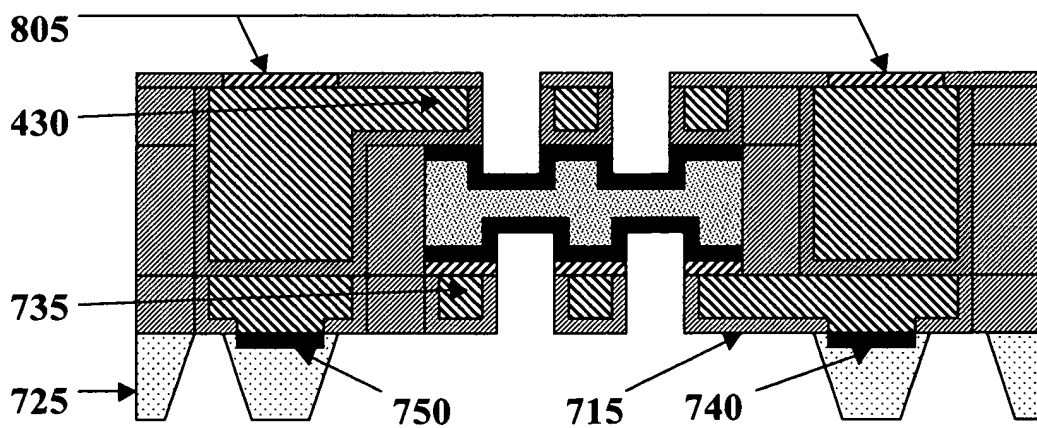


FIG. 34c

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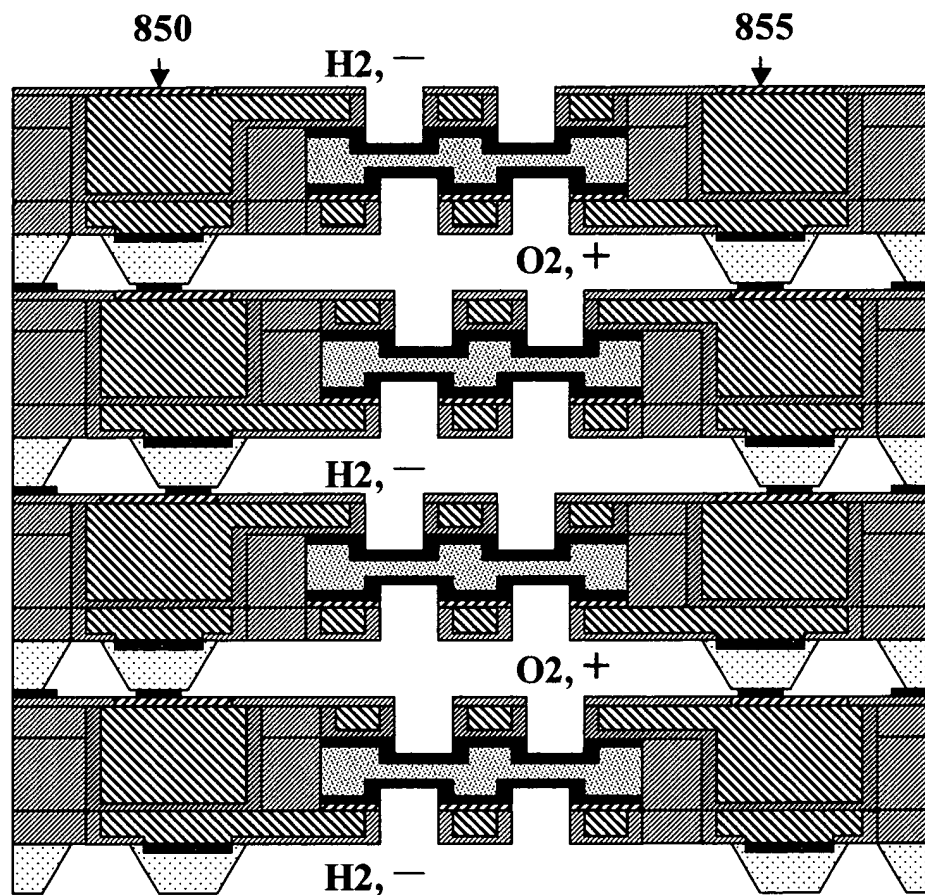


FIG. 35a

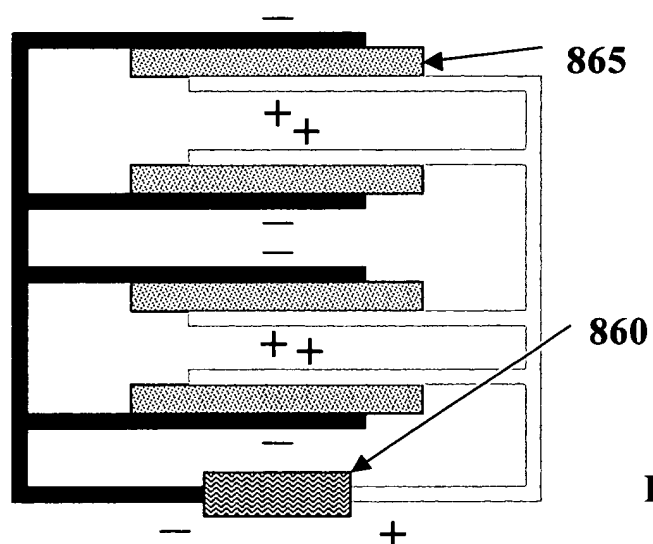
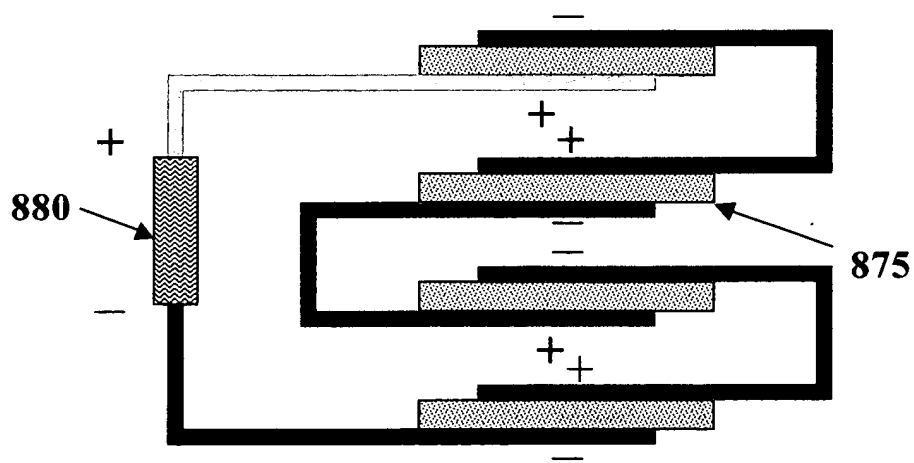
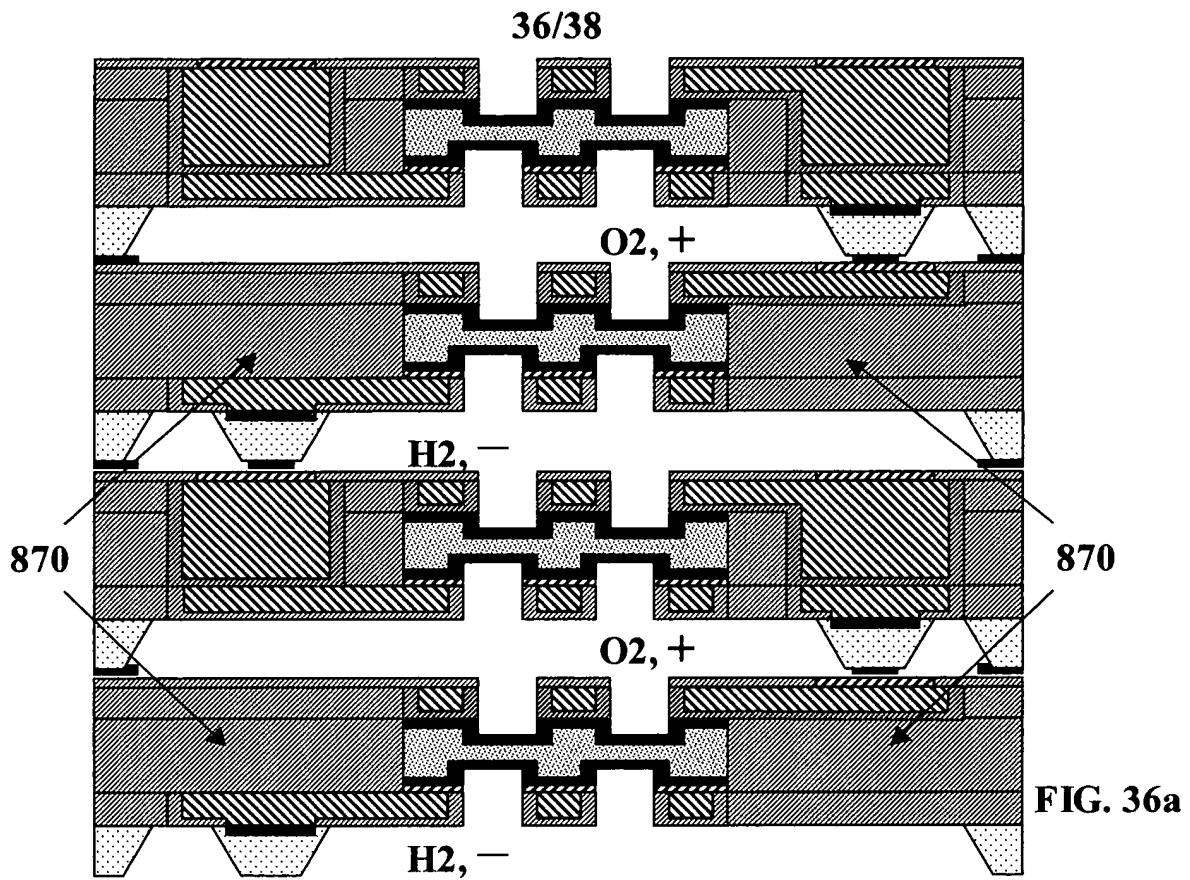


FIG. 35b



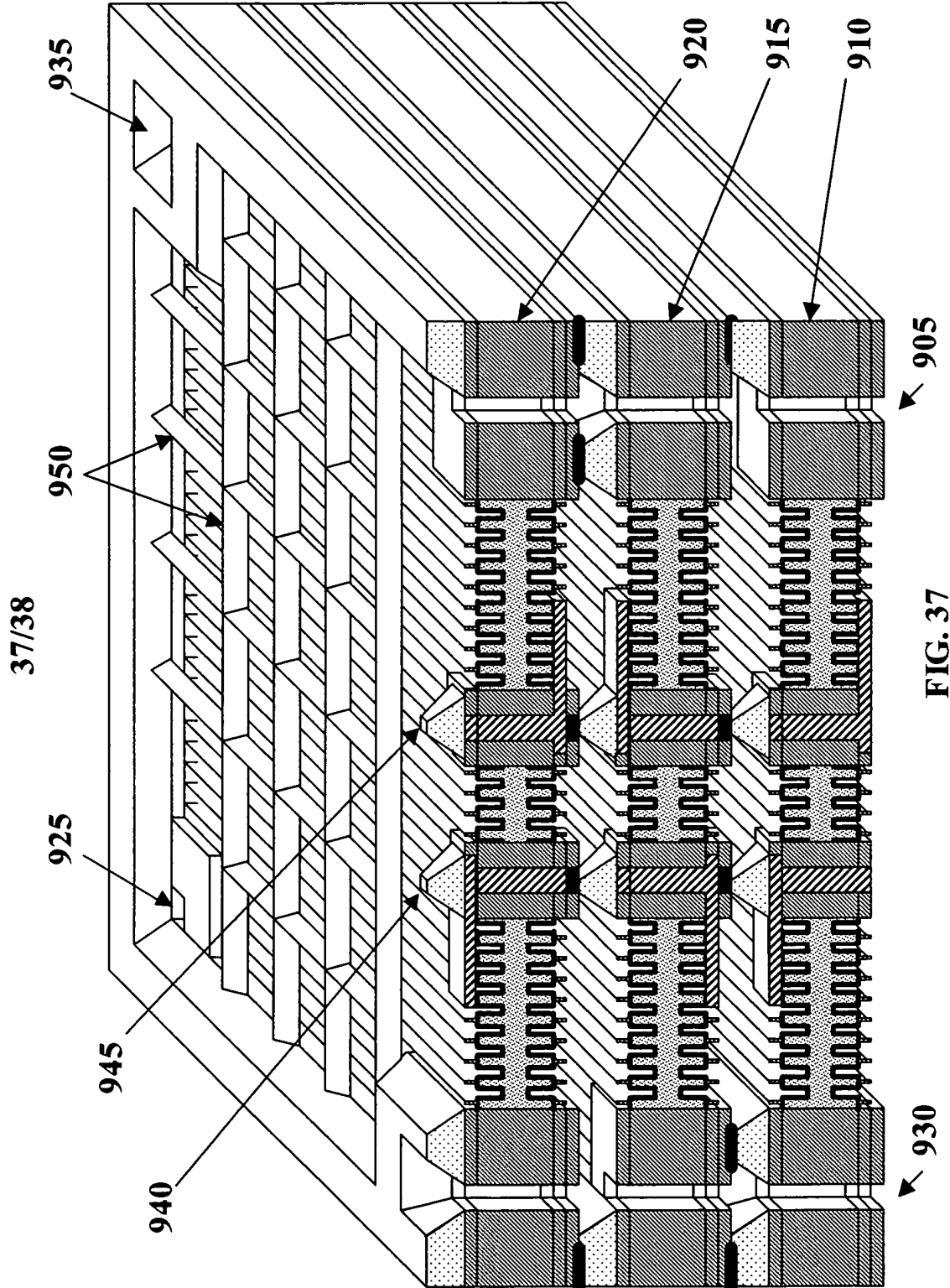


FIG. 37

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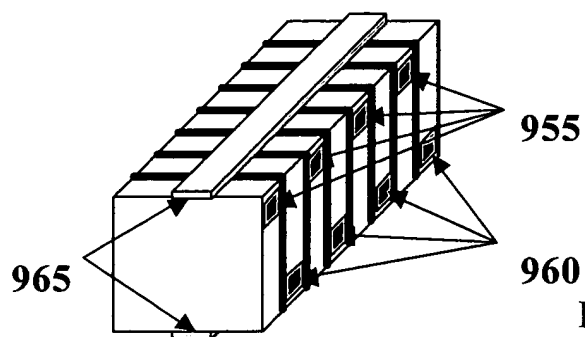


FIG. 38a

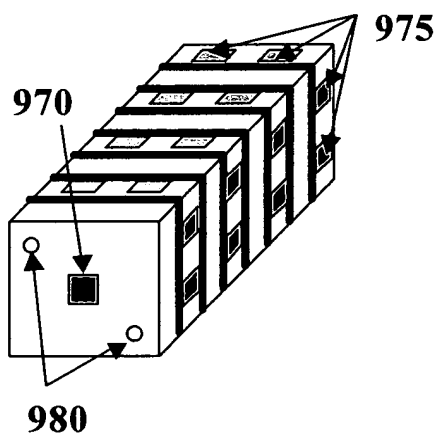


FIG. 38b

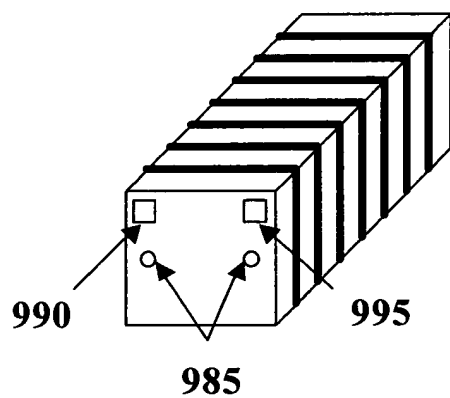


FIG. 38c